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Title: MODELING OF IUPQC BASED FUZZY CONTROLLER TO IMPROVE GRID-VOLTAGE REGULATION

Volume 06, Issue 07, Pages: 401-411.

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MODELING OF IUPQC BASED FUZZY CONTROLLER TO IMPROVE GRID-VOLTAGE REGULATION

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ABSTRACT-The iUPQC is a Unified Power Quality Conditioner in which the series converter emulates a sinusoidal current source and the shunt converter emulates a sinusoidal voltage source. The main causes of a poor power quality are harmonic currents, poor power factor, supply voltage variations, etc. In recent years the demand for the quality of electric power has been increased rapidly. Power quality problems have received a great attention nowadays because of their impacts on both utilities and customers. Voltage sag, swell, momentary interruption, under voltages, over voltages, noise and harmonics are the most common power quality disturbances. This approach provides indirect power quality compensation of the load voltage and the source current. By using this controller, beyond the conventional UPQC power quality features, including voltage sag/swell compensation, the iUPQC will also provide reactive power support to regulate not only the load-bus voltage but also the voltage at the grid-side bus. In other words, the iUPQC will work as a static synchronous compensator (STATCOM) at the grid side, while providing also the conventional UPQC compensations at the load or micro grid side. The proposed concept is implemented to Fuzzy logic controller by using Mat lab/simulink software.

Index Terms—iUPQC, microgrids, power quality, static synchronous compensator (STATCOM), unified power quality conditioner (UPQC).

I. INTRODUCTION

The Usage of power quality conditioners in the distribution system network has increased during the past years due to the steady increase of nonlinear loads connected to the Electrical grid. The current drained by nonlinear loads has a high harmonic content, distorting the voltage at the utility grid and consequently affecting the operation of critical loads [1-3]. By using a unified power quality conditioner (UPQC) it is possible to ensure a regulated voltage for the loads, balanced and with low harmonic distortion and at the same time draining undistorted currents from the utility grid, even if

the grid voltage and the load current have harmonic contents. The UPQC consists of two active filters, the series active filter (SAF) and the shunt or parallel active filter (PAF). The PAF is usually controlled as a non-sinusoidal current source, which is responsible for compensating the harmonic current of the load, while the SAF is controlled as a non-sinusoidal voltage source, which is responsible for compensating the grid voltage. Both of them have a control reference with harmonic contents, and usually, these references might be obtained through complex methods [4-6]. The



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line conditioner consists of two single phase current source inverters where the SAF is controlled by a current loop and the PAF is controlled by a voltage loop. In this way, both grid current and load voltage are sinusoidal, and therefore, their references are also sinusoidal. The aim of this is to propose dual three-phase four wire unified power quality conditioner (iUPQC) by using fuzzy logic in shunt active filter. It is to be used in the utility grid connection. Fuzzy logic control methodology been demonstrated to allow solving uncertain and vague problems. In this paper fuzzy logic controller is used for generation of switching pulses for PWM controllers [7]. The advantages of using fuzzy system are simplicity, ease of application, flexibility, speed and ability to deal with imprecision and uncertainties. Due to absorbing and supplying of active and reactive power in active filter, the capacitance voltage is not maintained constant. In literature many controllers are used for capacitance balancing, such as PI, PID, and fuzzy logic controller. In this paper, fuzzy control algorithm is used to balance the dc voltage of capacitance in order to improve the performance of controller. The proposed method is evaluated and tested under non sinusoidal source voltage conditions using Mat lab/Simulink software. The performance of UPQC depends on the characteristic of the active filters. The fuzzy logic controller is used in almost all sectors of industry and power systems and science and one among them is harmonic current and reactive power compensation [8]. In this way, both grid current and load voltage are sinusoidal, and therefore, their references are also sinusoidal. Some authors have applied this concept, using voltage source inverters in uninterruptable power supplies and in UPQC

[9]. In [10], this concept is called "dual topology of unified power quality conditioner" (iUPQC), and the control schemes use the p-q theory, requiring determination in real time of the positive sequence components of the voltages and thecurrents. The aim of this project is to propose a simplified control technique for a dual three-phase topology of a unified power quality conditioner (iUPQC) to be used in the utility grid connection. The proposed control scheme is developed in ABC reference frame and allows the use of classical control theory without the need for coordinate transformers and digital control implementation. The references to both SAF and PAFs are sinusoidal, dispensing the harmonic extraction of the grid current and load voltage [11-13].

II. EQUIPMENT APPLICABILITY

In order to clarify the applicability of the improved iUPQC controller, depicts electrical system with two buses in spotlight, i.e., bus A and bus B. Bus A is a critical bus of the power system that supplies sensitive loads and serves as point of coupling of a microgrid. Bus B is a bus of the microgrid, where nonlinear loads are connected, which requires premiumquality power supply. The voltages at buses A and B must be regulated, in order to properly supply the sensitive loads and the nonlinear loads. The effects caused by the harmonic currents drawn by the nonlinear loads should be mitigated, avoiding harmonic voltage propagation to bus A.The use of a STATCOM to guarantee the voltage regulation at bus A is not enough because the harmonic currents drawn by the nonlinear loads are not mitigated. On the other hand, a UPQC or an iUPQC between bus A and bus B can compensate the harmonic currents of the nonlinear loads and compensate the voltage at bus B, in terms of



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voltage harmonics, unbalance, and sag/swell. Nevertheless, this is still not enough to guarantee the voltage regulation at bus A. Hence, to achieve all the desiredgoals, a STATCOM at bus A and a UPQC (or an iUPQC) between buses A and B should be employed. However, the costs of this solution would be unreasonably high.An attractive solution would be the use of a modified iUPQC controller to provide also reactive power support to bus A, in addition to all those functionalities of this equipment, as presented. Note that the modified iUPQC serves as an intertie between buses A and B. Moreover, the microgrid connected to the bus B could be a complex system comprising distributed generation, energy management system, and other control systems involving microgrid, as well as smart gridconcepts.

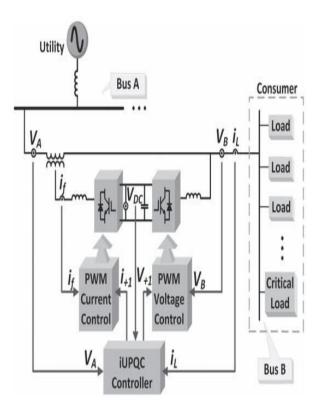


Fig.1. Modified iUPQC Configuration.

In summary, the modified iUPQC can provide the following functionalities:

- a) "Smart" circuit breaker as an intertie between the grid and the microgrid;
- b) Energy and power flow control between the grid and the microgrid (imposed by a tertiary control layer for the microgrid);
- c) Reactive power support at bus A of the power system;
- d) voltage/frequency support at bus B of the microgrid;
- e) Harmonic voltage and current isolation between bus A and bus B (simultaneous gridvoltage and load-current active filtering capability);
- Voltage imbalance f) and current compensation. According to the conventional iUPQC controller, the shunt converter imposes a controlled sinusoidal voltage at bus B, which corresponds to the aforementioned functionality (d). As a result, the shunt converter has no further degree of freedomin terms compensating activeor reactive-power variables to expand its functionality. On the other hand, the series converter of a conventional iUPQC uses only an active-power control variable p, in order to synthesize a fundamental sinusoidal current drawn from bus A, corresponding to the active powerdemanded by bus B. If the dc link of the iUPQC has no large energy storage system or even no energy source, the control variable p also serves as an additional active-power reference to the series converter to keep the energy inside the dc link of theiUPQC balanced. In this case, the losses in the iUPQC and the active power supplied by the shunt converter must be quickly compensated in the form of an additional active power injected by the series converter into the bus B.



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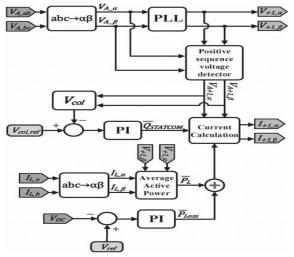


Fig.2. Novel iUPQC controller.

The iUPQC can serve as:

- a) "Smart" circuit breaker and as
- b) Power flow controller between the grid and the micro grid only if the compensating activeand reactive-power references of the series converter can be set arbitrarily. In this case, it is necessary to provide an energy source (or large energy storage) associated to the dc link of the iUPQC.The last degree of freedom represented by a reactive-power control variable \bar{q} for the series converter of the iUPQC. In this way, the iUPQC will provide reactive-power compensation like a STATCOM to the bus A of the grid. As it will beconfirmed, this functionality can be added into the controller without degrading all other functionalities of the iUPQC.

III. IMPROVED IUPQC CONTROLLER

A. Main Controller

Fig.1. depicts the iUPQC hardware and the measured units of a three-phase three-wire system that are used in the controller. Fig.2. shows the proposed controller. The controller inputs are the voltages at buses A and B, the current demanded by bus B (i_L), and the voltage v_{DC} of the common dc link. The outputs are the shunt-voltage reference and the series-current

reference to the pulse width modulation (PWM) controllers. The voltage and current PWM controllers can be as simple as those employed, or be improved further to better deal with voltage and current imbalance and harmonics. First, the simplified Clark transformation is applied to the measured variables. As example of this transformation, the grid voltage in the $\alpha\beta$ -reference frame can be calculated as

$$\begin{bmatrix} V_{A_\alpha} \\ V_{A_\beta} \end{bmatrix} = \begin{bmatrix} 1 & 1/2 \\ 0 & \sqrt{3}/2 \end{bmatrix} \begin{bmatrix} V_{A_ab} \\ V_{A_bc} \end{bmatrix}$$
(1)

The shunt converter imposes the voltage at bus B. Thus, it is necessary to synthesize sinusoidal voltages with nominal amplitude and frequency. Consequently, the signals sent to the PWM controller are the phase-locked loop (PLL) outputs with amplitude equal to 1 p.u. In the original iUPQC approach as presented, the shunt-converter voltage reference can be either the PLL outputs or the fundamental positivesequence component V_{A+1} of the grid voltage (bus A in Fig.1.). The use of V_{A+1} in the controller is useful to minimize the circulating power through the series and shunt converters, under normal operation, while the amplitude of the grid voltage is within an acceptable range of magnitude. However, this is not the case here, in the modifiediUPQC controller, since now the grid voltage will be also regulated by the modified iUPQC. In other words, both buses will be regulated independently to track their reference values.The series converter synthesizes the current drawn from the grid bus (bus A). In the original approach of iUPQC, this current is calculated through the average active power required by the loads \bar{P}_L plus the power \bar{P}_{Loss} . The load active power can be estimated by



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$$P_L = V_{+1}\underline{\ }\alpha \cdot i_L\underline{\ }\alpha + V_{+1}\underline{\ }\beta \cdot i_L\underline{\ }\beta$$
 (2)

Where $i_{L_{-}\alpha}$, $i_{L_{-}\beta}$ are the load currents, and $V_{+1_{-}\alpha}$, $V_{+1_{-}\beta}$ are the voltage references for the shunt converter. A low-pass filter is used to obtain the average active power (\bar{P}_L) .

The losses in the power converters and the circulating power to provide energy balance inside the iUPQC are calculated indirectly from the measurement of the dc-link voltage. In other words, the power signal \bar{P}_{Loss} is determined by a proportional—integral (PI) controller (PI block in Fig. 2), by comparing them ensured dc voltage VDC with its reference value. The additional control loop to provide voltage regulation like a STATCOM at the grid bus is represented by the control signal $\bar{Q}_{STATCOM}$ in Fig. 2. This control signal is obtained through a PI controller, in which the input variable is the error between the reference value and the actual aggregate voltage of the grid bus, given by

$$V_{\text{col}} = \sqrt{V_{A+1}^2 + V_{A+1}^2}.$$
 (3)

The sum of the power signals \bar{P}_L and \bar{P}_{Loss} composes the active-power control variable for the series converter of the iUPQC (\bar{p}) described in Section II. Likewise, $\bar{Q}_{STATCOM}$ is the reactive-power control variable q. Thus, the current references $i+1\alpha$ and $i+1\beta$ of the series converter are determined by

$$\begin{bmatrix} i_{+1_\alpha} \\ i_{+1_\beta} \end{bmatrix} = \frac{1}{V_{A+1_\alpha}^2 + V_{A+1_\beta}^2} \begin{bmatrix} V_{A+1_\alpha} & V_{A+1_\beta} \\ V_{A+1_\beta} & -V_{A+1_\alpha} \end{bmatrix} \times \begin{bmatrix} \overline{P}_L + \overline{P}_{\text{Loss}} \\ \overline{Q}_{\text{STATCOM}} \end{bmatrix}$$
(4)

B. Power Flow in Steady State

The following procedure, based on the average power flow, is useful for estimating the power ratings of the iUPQC converters. For combined series—shunt power conditioners, such as the UPQC and the iUPQC, only the voltage sag/swell disturbance and the power factor (PF)

compensation of the load produce a circulating average power through the power conditioners. According to Fig.3, the compensation of a voltage sag/swell disturbance at bus B causes a positive sequence voltage at the coupling transformer $(V_{\text{series}} \neq 0)$, since $VA \neq VB$. Moreover, V_{series} and i_{PB} in the coupling transformer leads to a circulating active power \bar{P}_{inner} in theiUPQC.

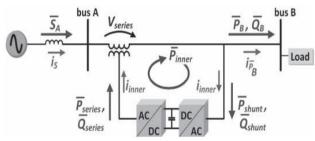


Fig.3. iUPQC power flow in steady-state.

Additionally, the compensation of the load PF increases the current supplied by the shunt converter. The following analysis is valid for an iUPQC acting like a conventional UPQC or including the extra compensation like a STATCOM.First, the circulating power will be calculated when the iUPQC is operating just like a conventional UPQC. Afterward, the equations will include the **STATCOM** functionality to the grid bus A. In both cases, it will be assumed that the iUPQC controller is able to force the shunt converter of the iUPQC to generate fundamental voltage always in phase with the grid voltage at bus A. For simplicity, the losses in the iUPQC will be neglected.For the first case, the following average powers in steady state can be determined:

$$\overline{S}_A = \overline{P}_B \tag{5}$$

$$\overline{Q}_{\mathrm{shunt}} = -\overline{Q}_B$$
 (6)



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$$\overline{Q}_{\text{series}} = \overline{Q}_A = 0 \text{ var}_{(7)}$$

$$\overline{P}_{\text{series}} = \overline{P}_{\text{shunt}}$$
 (8)

Where \bar{S}_A and \bar{Q}_A are the apparent and reactive power injected in the bus A; \bar{P}_B and \bar{Q}_B are the active and reactive power injected in the bus B; \bar{P}_{Shunt} and \bar{Q}_{Shunt} are the active and reactive power drained by the shunt converter; \bar{P}_{Series} and $ar{Q}_{Series}$ are the active and reactive power supplied by the series converter, respectively. Equations (5) and (8) are derived from the constraint of keeping unitary the PF at bus A. In this case, the current passing through the series converter is responsible only for supplying the load active power, that is, it is in phase (or counter phase) with the voltages V_A and V_B . Thus, (7) can be stated. If a voltage sag or swell occurs, \bar{P}_{Series} and \bar{P}_{Shunt} will not be zero, and thus, an inner-loop current (i_{inner}) will appear. The series and shunt converters and the aforementioned circulating active power (\bar{P}_{inner}) flow inside the equipment. It is convenient to define the following sag/swell factor. Considering VN as the nominal voltage

$$k_{\text{sag/swell}} = \frac{|\dot{V}_A|}{|\dot{V}_N|} = \frac{V_A}{V_N}$$
 (9)

From (5) and considering that the voltage at bus B is kept regulated, i.e., VB = VN, it follows that

$$\sqrt{3} \cdot k_{\text{sag/swell}} \cdot V_N \cdot i_S = \sqrt{3} \cdot V_N \cdot i_{P_B}$$

$$i_S = \frac{i_{P_B}}{k_{\text{sag/swell}}} = i_{\overline{P}_B} + i_{\text{inner}}$$
(10)

$$i_{\text{inner}} = \left| i_{P_B} \left(\frac{1}{K_{\text{sag/swell}} - 1} \right) \right|$$
(11)

The circulating power is given by

$$\overline{P}_{\text{inner}} = \overline{P}_{\text{series}} = \overline{P}_{\text{shunt}} = 3(V_B - V_A)(i_{P_B} + i_{\text{inner}})$$
(12)

From (11) and (12), it follows that

$$\overline{P}_{\text{inner}} = 3(V_N - V_A) \left(\frac{\overline{P}_B}{3V_N} \frac{1}{k_{\text{sag/swell}}} \right)$$
(13)

$$\overline{P}_{\text{inner}} = \overline{P}_{\text{series}} = \overline{P}_{\text{shunt}} = \frac{1 - K_{\text{sag/swell}}}{k_{\text{sag/swell}}} \overline{P}_{B}$$
(14)

Thus, (14) demonstrates that \bar{P}_{inner} depends on the active power of the load and the sag/swell voltage disturbance. In order to verify the effect on the power rate of the series and shunt converters, load system $\overline{S}_B = \sqrt{\overline{P}_B^2 + \overline{Q}_B^2} = 1 \text{ p.u.}$ with PF ranging from 0 to 1 was considered. It was also considered the sag/swell voltage disturbance at bus A ranging $k_{\text{sag}}/_{\text{swell}}$ from 0.5 to 1.5. In this way, the power rating of the series and shunt converters are obtained through (6)–(8) and (14). The apparent power of the series and shunt power converters. In these figures, the $k_{\text{sag}}/_{\text{swell}}$ -axis and the PFaxis are used to evaluate the power flow in the series and shunt power converters according to the sag/swell voltage disturbance and the load power consumption, respectively. The power flow in the series converter indicates that a high power is required in case of sag voltage disturbance with high active power load consumption. In this situation, an increased \bar{P}_{inner} arises and high rated power converters are



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necessary ensure the disturbance compensation. in case Moreover, compensating sag/swell voltage disturbance with high reactive power load consumption, only the shunt converter has high power demand, since \bar{P}_{inner} decreases. It is important to highlight that, for each PF value, the amplitude of the apparent power is the same for capacitive or inductive loads. In other words, the same for \bar{Q}_B capacitive or inductive. If the performs iUPOC all original **UPOC** functionalities together with the STATCOM functionality, the voltage at bus A is also regulated with the same phase and magnitude, that is, $\dot{V}_A = \dot{V}_B = \dot{V}_N$, and then, the positive sequence of the voltage at the coupling transformer is zero (\bar{V}_{series} =0). Thus, in steady state, the power flow is determined by

$$\overline{S}_{A} = \overline{P}_{B} + \overline{Q}_{\text{STATCOM}} (15)$$

$$\overline{Q}_{\text{STATCOM}} + \overline{Q}_{\text{series}} = \overline{Q}_{\text{shunt}} + \overline{Q}_{B}$$
(16)

$$\overline{Q}_{\text{series}} = 0 \text{ var}$$
 (17)

$$\overline{P}_{\text{series}} = \overline{P}_{\text{inner}} = 0 \text{ W}$$
 (18)

Where $\bar{Q}_{STATCOM}$ is the reactive power that provides voltage regulation at bus A. Ideally, the STATCOM functionality mitigates the inner-loop active power flow (\bar{P}_{inner}) , and the power flow in the series converter is zero. Consequently, if theseries converter is properly designed along with the coupling transformer to synthesize the controlled currents I_{+1_α} and I_{+1_β} , as shown in Fig. 5.3, then a lower power

converter can be employed. Contrarily, the shunt converter still has to provide full reactive power of the load and also to drain the reactive power injected by the series converter to regulate the voltage at bus A.

IV.FUZZY LOGIC CONTROL

L. A. Zadeh presented the first paper on fuzzy set theory in 1965. Since then, a new language was developed to describe the fuzzy properties of reality, which are very difficult and sometime even impossible to be described using conventional methods. Fuzzy set theory has been widely used in the control area with some application to power system [5]. A simple fuzzy logic control is built up by a group of rules based on the human knowledge of system behavior. Matlab/Simulink simulation model is built to study the dynamic behavior of converter. Furthermore, design of fuzzy logic controller can provide desirable both small signal and large signal dynamic performance at same time, which is not possible with linear control technique. Thus, fuzzy logic controller has been potential ability to improve the robustness of compensator. The basic scheme of a fuzzy logic controller is shown in Fig 4 and consists of four principal components such as: a fuzzy fication interface, which converts input data into suitable linguistic values; a knowledge base, which consists of a data base with the necessary linguistic definitions and the control rule set; a decision-making logic which, simulating a human decision process, infer the fuzzy control action from the knowledge of the control rules and linguistic variable definitions; a de-fuzzification interface which yields non fuzzy control action from an inferred fuzzy control action [10].



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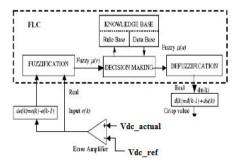


Fig.4.Block diagram of the Fuzzy Logic Controller (FLC) for proposed converter.

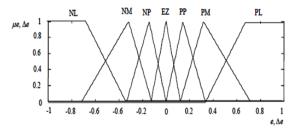


Fig.5. Membership functions for Input, Change in input, Output.

Rule Base: the elements of this rule base table are determined based on the theory that in the transient state, large errors need coarse control, which requires coarse in-put/output variables; in the steady state, small errors need fine control, which requires fine input/output variables. Based on this the elements of the rule table are obtained as shown in Table 1, with 'Vdc' and 'Vdc-ref' as inputs.

Δe e	NL	NM	NS	EZ	PS	PM	PL
NL	NL	NL	NL	NL	NM	NS	EZ
NM	NL	NL	NL	NM	NS	EZ	PS
NS	NL	NL	NM	NS	EZ	PS	PM
EZ	NL	NM	NS	EZ	PS	PM	PL
PS	NM	NS	EZ	PS	PM	PL	PL
PM	NS	EZ	PS	PM	PL	PL	PL
PL	NL	NM	NS	EZ	PS	PM	PL

V.MATLAB/SIMULATION RESULTS

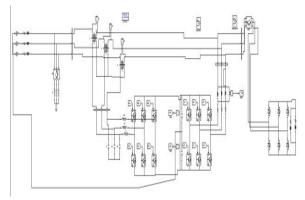


Fig 6 Matlab/simulation circuit for conventional method of iUPQC

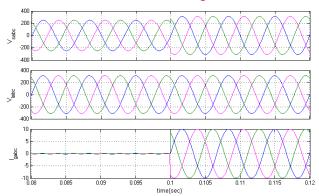


Fig 7 simulation wave form of iUPQC response at no load condition grid voltages *VA*, load voltages *VB*, and grid currents

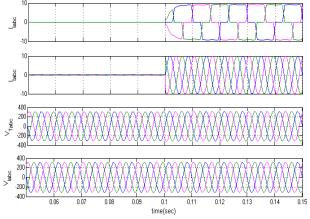


Fig 8 simulation wave form of iUPQC transitory response during three phase diode rectifier (a) load current (b) grid current (c) grid voltage (d) load voltage



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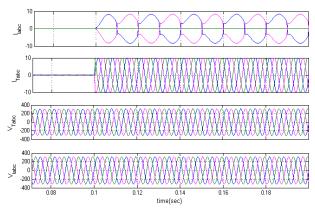
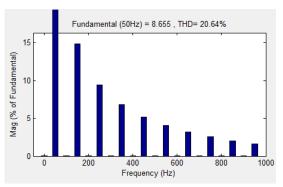


Fig 9 simulation wave form of iUPQC transitory response during the connection of a two phase diode rectifier (a) load current (b) grid current (c) grid voltage (d) load voltage



THD of Load currents with PI control

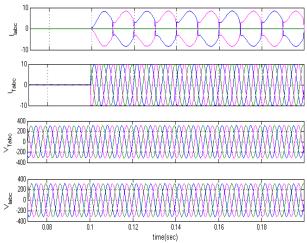
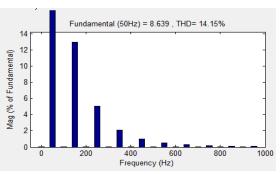


Fig 10 simulation wave form of iUPQC response at no load condition grid voltages *VA*, load voltages *VB*, and grid currents with fuzzy controller



THD of Load currents with Fuzzy controller **VI.CONCLUSION**

The results is through with iUPQC using Fuzzy Logic Controller and design with Matlab/Simulation Technique in ABC reference frame and using series active filter and parallel active to compensate the harmonics from nonlinear load current. A fuzzy code designed to control something, which may be software or hardware is used from small circuits to large mainframes. First create the membership values (fuzzy) and specify the rule table and also determine your procedure for defuzzifying the result. A proposed scheme of iUPQC using fuzzy controller in ABC reference frame of both the active filters and their control loops are generated by a digital signal processor (DSP) and to related to other proposed controls its utilization is better for a sinusoidal reference and to eliminate the harmonic from source to load.

REFERENCES

[1] K. Karanki, G. Geddada, M. K. Mishra, and B. K. Kumar, "A modified three-phase four-wire UPQC topology with reduced DC-link voltage rating," IEEE Trans. Ind. Electron., vol. 60, no. 9, pp. 3555–3566, Sep. 2013.

[2] V. Khadkikar and A. Chandra, "A new control philosophy for a unified power quality conditioner (UPQC) to coordinate load-reactive power demand between shunt and series



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- inverters," IEEE Trans. Power Del., vol. 23, no. 4, pp. 2522–2534, Oct. 2008.
- [3] K. H. Kwan, P. L. So, and Y. C. Chu, "An output regulation-based unified power quality conditioner with Kalman filters," IEEE Trans. Ind. Electron., vol. 59, no. 11, pp. 4248–4262, Nov. 2012.
- [4] A. Mokhtatpour and H. A. Shayanfar, "Power quality compensation as well as power flow control using of unified power quality conditioner," in Proc. APPEEC, 2011, pp. 1–4.
- [5] J. A. Munoz et al., "Design of a discrete-time linear control strategy for a multi cell UPQC," IEEE Trans. Ind. Electron., vol. 59, no. 10, pp. 3797–3807, Oct. 2012.
- [6] V. Khadkikar and A. Chandra, "UPQC-S: A novel concept of simultaneous voltage sag/swell and load reactive power compensations utilizing series inverter of UPQC," IEEE Trans. Power Electron., vol. 26, no. 9, pp. 2414–2425, Sep. 2011.
- [7] V. Khadkikar, "Enhancing electric power quality using UPQC: A comprehensive overview," IEEE Trans. Power Electron., vol. 27, no. 5, pp. 2284–2297, May 2012.
- [8] L. G. B. Rolim, "Custom power interfaces for renewable energy sources," in Proc. IEEE ISIE, 2007, pp. 2673–2678.
- [9] N. Voraphonpiput and S. Chatratana, "STATCOM analysis and controller design for power system voltage regulation," in Proc. IEEE/PES Transmiss. Distrib. Conf. Exhib.—Asia Pac., 2005, pp. 1–6.
- [10] J. J. Sanchez-Gasca, N. W. Miller, E. V. Larsen, A. Edris, and D. A. Bradshaw, "Potential benefits of STATCOM application to improve generation station performance," in Proc. IEEE/PES Transmiss. Distrib. Conf. Expo., 2001, vol. 2, pp. 1123–1128.

- [11] A. P. Jayam, N. K. Ardeshna, and B. H. Chowdhury, "Application of STATCOM for improved reliability of power grid containing a wind turbine," in Proc. IEEE Power Energy Soc. Gen. Meet.—Convers. Del. Elect. Energy 21st Century, 2008, pp. 1–7.
- [12] C. A Sepulveda, J. A Munoz, J. R. Espinoza, M. E. Figueroa, and P. E. Melin, "All-on-chip dq-frame based D-STATCOM control implementation in a low-cost FPGA," IEEE Trans. Ind. Electron., vol. 60, no. 2, pp. 659–669, Feb. 2013.
- [13] B. Singh and S. R. Arya, "Back-propagation control algorithm for power quality improvement using DSTATCOM," IEEE Trans. Ind. Electron., vol. 61, no. 3, pp. 1204–1212, Mar. 2014.

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