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Title **PHASESHIFTED CARRIER BASED HARMONIZED SINUSOIDALPWM TECHNIQUESFOR DROPPED H-BRIDGE MULTI LEVEL INVERTERS**

Volume 08, Issue 09, Pages: 528–545.

Paper Authors

SK AFRIN, SK NASEEMA, SD REEMA FARHEEN

Anu Bose Institute of Technology K.S.P Road, New paloncha, Bhadradi Kothagudem, Telangana, India



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PHASESHIFTED CARRIER BASED HARMONIZED SINUSOIDAL PWM TECHNIQUES FOR DROPPED H-BRIDGE MULTI LEVEL INVERTERS

SK AFRIN¹, SK NASEEMA², SD REEMA FARHEEN³

^{1,2,3}UG Students, Dept. of Electrical and Electronics Engineering Anu Bose Institute of Technology
KSP Road, Newpaloncha, BhadradriKothagudem, Telangana, India.

afrinshaik199902@gmail.com¹, naseema4899@gmail.com², princessonu46@gmail.com³

Abstract:-This paper assessments synchronization approach fell H-Bridge amized inverter (CHBMLI) topologies with conveyor based sinusoidal stage moved heartbeat width balance (PSPWM) methodology. In PSPWM framework an alternate transporter is used for each H-Bridge. The transporters are generally stage moved from each other by π/x rad (x =No. of H-Bridges) for unipolar PWM. With the conveyor repeat being an entire number (odd/even) various of the fundamental repeat, it is seen that the spots of zero crossing points of the transporters concerning the zero convergences of voltage references accept a noteworthy activity for keeping up quarter wave symmetry among amized inverter (MLI) post voltage waveforms. This paper demonstratively shows the conditions for half wave symmetry and quarter wave symmetry and likely checks those conditions for PSPWM framework with a five level CHBMLI lab model.

Index Terms:- Cascaded H-Bridge Multilevel Inverter, Phase shifted carrier based PWM, Synchronous PWM, Half Wave Symmetry, Quarter Wave Symmetry.

I. INTRODUCTION

With the growing solicitation of medium voltage and high power drives, the use of stunned inverters is well ordered winding up progressively critical. This is a direct result of reduced voltage weight on semiconductor devices, diminished music in inverter yield voltage and lesser electromagnetic impedances. For medium voltage and high power applications, the trading repeat and device evaluations are confined [1]-[3]. Extending the power rating by restricting the trading repeat, while up 'til now keeping up reasonable power quality is a huge need and a relentless test [2]-[3]. Therefore, the use of amized inverters fittingly passes on the stress. Among the unmistakable amized inverter topologies, the

fell H-Bridge (CHB) topology is a favored choice for medium voltage drives for its disposition and this is furthermore the target converter for the proposed PWM methodology in this paper. In high power and medium voltage applications, the power converters work at low trading repeat. This is especially portrayed in the composition as low heartbeat extent action of the converters. The applications contain balance drives (both VSI and CSI energized drives), cross section applications (as bi-directional converters, dynamic power channels, etc. As the beat extent is less in these power converters, lower demand music including sub-sounds are introduced in line streams achieving

higher full scale symphonious mutilation (THD). Hereafter, synchronization among PWM voltages is fundamental. Close by synchronization, the PWM voltage should keep up halfwave, quarterwave and three phase symmetries [4]-[5]. Reference [6] exhibits the upsides of keeping up the above waveformsymmetries.The synchronization ensures the PWMvoltageisfree from the sub-music. Threephase symmetry among the PWMvoltages ensures the major and the music arebalanced and moreover free from dc offset. Also, three phase symmetry similarly ensures that, the triplen sounds are in stage and appear as zero gathering voltage with the advantage of not adding to the swell current. Half wave symmetry ensures the PWM voltages are free from even solicitation music. Notwithstanding the way that quarter wave symmetry does not get rid of any sounds, it ensures that the present fundamental and music have simply sine part and discards the probability of stage botch between the reference and the basic yield voltage. Reference [7] proposes a perfect PWM scheme for two level inverters to reduce current music with detached figurings of trading plan. This improved PWM framework can moreover be extended to stunned inverters. Reference [8] exhibits the utilization of synchronized perfect PWM methodology for a fell nine level inverter, where the typical device trading repeat is confined to assessed head repeat. Reference [9] considers the presentations of five level and seven level NPC inverters with synchronous perfect PWM technique. In any case, the synchronized perfect PWM technique is a separated tally based framework. The trading focuses are pre-decided tolerating enduring state condition and this requires amassing of gigantic data for better exactness. Reference [10] proposes a

bearing after control for three level NPC with synchronous perfect PWM methodology. Reference proposes the model insightful heartbeat model control for a five level NPC inverter with streamlined PWM framework. Specific consonant transfer and explicit symphonious help PWM strategies are various choices. Reference exhibits the use of SHEPWM methodology for course amazed inverters, however shows the use of SHMPWM technique. Regardless, they are furthermore detached estimation based PWM methodology and require inquiry tables for execution, along these lines the more microcontroller memory space. References show the utilization of online consonant compensation intend to improve the existingSHEPWM technique for high power converters. These references generally revolve around cross section related high power converters (for instance current source rectifiers). All the above stunned inverter PWM frameworks (SOPWM, SHEPWM and SHMPWM Techniques, leaving on the web consonant compensation plot) rely upon disengaged estimations. The figuring multifaceted design increases with augmentation in number of voltage levels or addition in the amount of reward focuses at lower equalization records. Regardless, the carrier based PWM techniques are self-sufficient of motor parameters, free of progress and does not require detached calculation or investigate tables to make ending focuses. In this manner, conveyor based PWM technique is fitting to stunned inverters in spite of the way that the symphonious substance of the inverter yield post voltage are not improved. Two transporter based PWM frameworks are available recorded as a hard copy for MLIs. They are:- (I) Level moved PWM methodology (LSPWM technique) (ii) Phase moved PWM

procedure (PSPWM framework). Nevertheless, the genuine test for the transporter based PWM systems for MLIs is to position the zero crossing points of bearers' with concession to the zero convergences of voltage references so different symmetries among post voltage waveforms can be kept up. For two-level inverters, synchronization with different symmetries is reachable if the zero crossing points of the transporter matches with the zero convergences of the voltage references and the extent p' ($p = f_c/f_s$ where $f_c =$ bearer recurrence and $f_s =$ voltage reference recurrence) is kept up to be an odd number (various of 3). The transporter synchronization with the voltage references is satisfactory for the Level Shifted PWM (LSPWM) strategy for CHBMLIs, as only a solitary synchronous conveyor is sufficient to complete various voltage levels. Thusly, the post voltage keeps up all the basic properties of an ideal synchronous PWM technique. Regardless, the power course and typical device trading frequencies of different H-Bridges are uncommon. Regardless, this circumstance is absolutely phenomenal for the PSPWM framework, as various stage moved synchronous transporters are used for different H-Bridges. Along these lines, it is hard to arrange the zero crossing points of each transporter with the voltage reference zero convergences. In this way, the spots of the zero convergences of voltage references concerning the zero crossing points of transporters expect a huge activity for keeping up different central properties of an ideal synchronous PWM, as communicated in the past segment. This paper basically deals with the insightful assessments for keeping up half wave symmetry and quarter wave symmetry among CHBMLI shaft voltage waveform with synchronous

sinusoidal PSPWM methodology. The transporters used for the assessment in this paper are created from the brief voltage references, as in [17]-[19] and reliably keep up an entire number extent p .

II. CONDITIONS OF PHASE SHIFTED SYNCHRONOUS PWM FOR CHBMLI

The unipolar PWM system is utilized for producing door beats for every H-Bridge. The H-Bridge shaft voltage fluctuates between 0 to $+V_{dc}$ when the indication of the voltage reference is sure and changes between 0 to $-V_{dc}$ when the indication of the voltage reference is negative (where V_{dc} is the information DC interface voltage of the H-Bridge). For actualizing unipolar PWM in a H-Bridge 1 (Fig.1.(a)), a positive voltage reference $R1$ is utilized to create entryway beats for switches $S11$ and $S12$ of leg 1 and a negative voltage reference $R2$ is utilized to produce door beats for switches $S13$ and $S14$ of leg 2. A typical bearer $CHB1$ is utilized for both the legs so as to produce door beats. The resultant yield post voltage V_{HB1} is the arithmetical summation of individual leg voltages for example $V_{HB1} = V_{HBL1} - V_{HBL2}$.

A. Verification of Synchronization

The synchronization between voltage reference and essential segment of inverter yield shaft voltage can be kept up if the synchronous bearer recurrence is (n being any whole number) times the voltage reference recurrence. By keeping up the above condition the convergence focuses between the transporter and the voltage reference (for example in a key cycle of the voltage reference) rehash after 2π rad (for example the following sequential principal cycle of the voltage reference), thus synchronization is kept up.

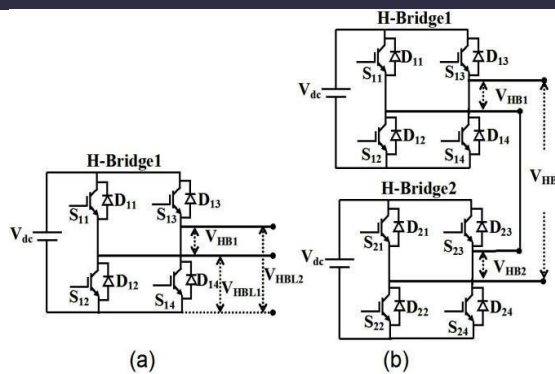


Fig.1(a) Single H-Bridge ; (b) Double Cascaded H-Bridges.

B.Verification of ThreePhase Symmetry & HalfWaveSymmetry

With synchronous bearers having 3n times the essential recurrence (n being any number) being utilized for the PSPWM strategy, three stage symmetry is constantly kept up among individual H-Bridge yield shaft voltage waveforms. Additionally, from Fig.2.(a) it tends to be seen that, for an odd number proportion between the bearer and major recurrence, the locale from π rad to 2π rad is proportionate to a perfect representation of the area from 0 rad to π rad as for axis (θ). Consequently, the crossing point guides C1 toward C6 are the perfect representations of focuses from C7 to C12 separately. Truth be told, for an odd whole number proportion, singular shaft voltages of both the legs (V_{HBL1} and V_{HBL2}) of one H-connect keep up half wave symmetry and henceforth their distinction additionally keeps up half wave symmetry. In the event that the proportion is even, at that point, singular shaft voltages of both the legs (V_{HBL1} and V_{HBL2}) don't keep up half wave symmetry. In any case, the waveform of V_{HBL1} from 0 rad to π rad is a definite imitation of V_{HBL2} from π rad to 2π rad and the other way around. Thus, their distinction (for example $V_{HB1} = V_{HBL1} - V_{HBL2}$) keeps up half wave symmetry. Henceforth, for a H-connect, the half wave symmetry is

fulfilled for any transporter having its recurrence equivalent to number (odd/even) various of the principal recurrence. Subsequently, it very well may be presumed that the inverter shaft voltage waveform keeps up three stage and half wave symmetry for bearers having 3n times the principal recurrence (n being any odd/much number). It is along these lines just important to decide the conditions for quarter wave symmetry in the inverter yield shaft voltage waveform.

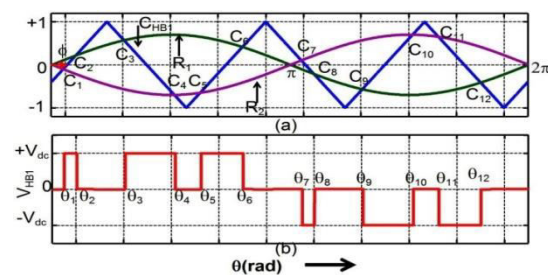


Fig. 2.(a) Carrier C_{HB1} lagging the normalized voltage references R₁ & R₂ by Φ rad ($f_c/f_s=3$); (b) pole voltage V_{HB1}.

C.Determination of conditions for Quarter Wave Symmetry 1) Single H-Bridge

The quarter wave symmetry among shaft voltage waveform of a H-Bridge (Fig.1.(a)) can be kept up, if the positive zero convergence of the voltage reference relates with the positive zero crossing point of the conveyor. The institutionalized fundamental voltage references are sinewaves with their adequacy not actually or comparable to one. The bearers are triangular waves with the size of their positive & negative apex comparable to one. Positive zero convergence suggests, the prompt estimations of these irregular waveforms have completed their negative half cycles and become zero going before entering the positive half cycle. The transporter can be of two sorts. They are: - (I) In stage transporter (positive zero crossing point of positive

voltage reference matches with the positive zero convergence of the conveyor) (ii) Out of stage carrier (positive zero crossing point of the positive voltage reference relates with the negative zero convergence of the transporter). For transporter repeat being an odd diverse of the basic repeat, particular shaft voltages of both the legs (VHBL1 and VHBL2) keep up quarter wave symmetry guaranteeing quarter wave symmetry of the extension yield voltage VHB1. For bearer recurrence being an even numerous of the principal recurrence, singular shaft voltages of both the legs (VHBL1 and VHBL2) don't keep up quarter wave symmetry. Be that as it may, their distinction $VHB1 = VHBL1 - VHBL2$ keeps up quarter wave symmetry. On the off chance that the bearer is stage deferred by $\pi/2$ rad (where 2π rad is one transporter period), at that point the turn around marvel occurs. Presently, for transporter recurrence being an even numerous of the essential recurrence, singular post voltages of both the legs (VHBL1 and VHBL2) keep up quarter wave symmetry. While, for bearer recurrence being an odd various of the basic recurrence, singular shaft voltages of both the legs (VHBL1 and VHBL2) don't keep up quarter wave symmetry. Be that as it may, their distinction $VHB1 = VHBL1 - VHBL2$ keeps up quarter wave symmetry. This can likewise be determined numerically as appeared in the accompanying talk. As a particular case the bearer recurrence is taken to be multiple times the crucial recurrence. From Fig.2, for keeping up quarter wave symmetry of the post voltage waveform VHB1, the imperative to be fulfilled among voltage reference and bearer crossing point directs $C1$ toward $C6$ is given by (1).

$$\theta_{7-l} = \pi - \theta_l \quad \text{for } l = 1, 2 \text{ and } 3 \quad (1)$$

For $l=1$, the estimations of θ_1 and θ_6 , at focuses $C1$ and $C6$ can be discovered by likening the conditions of voltage reference's and the carrier's. A little advance is appeared underneath to decide the conditions of voltage references and transporters at focuses $C1$ and $C6$ with the assistance of the conditions (2), (3), (4) and (5). Fig.3 demonstrates the broadened perspective on voltage references $R1$ and $R2$ alongside transporter $CHB1$. The point $C1$ (whose follow is θ_1 along x-pivot (θ)) is the crossing point between voltage reference $R2$ and line AB which is one piece of the transporter $CHB1$. The condition of the voltage reference at $C1$ can be composed as

$$z_1 = -m \sin \theta_1 \quad (2)$$

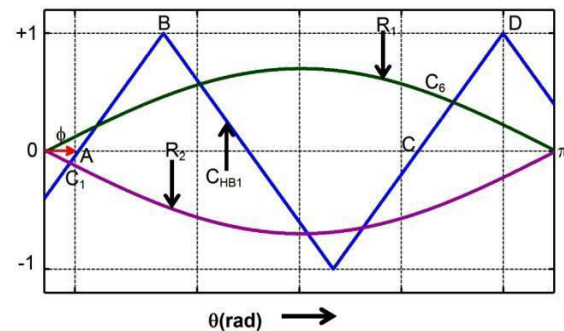


Fig. 3. Carrier C_{HB1} lagging the normalized voltage references R_1 and R_2 by Φ rad ($f_c/f_s=3$).

The co-ordinates of the points A and B are $(\Phi, 0)$ and $(\Phi+\pi/6, 1)$ respectively. Hence, the equation of the line AB at point C_1 can be written as (3).

$$z_1 = \left(\frac{6}{\pi}\right)(\theta_1 - \Phi) \quad (3)$$

Similarly, the point C_6 is the intersection point between R_1 and line CD (which is a portion of carrier C_{HB1}).

The equation of the voltage reference at C_6 can be written as (4).

$$z_6 = m \sin \theta_6 \quad (4)$$

The co-ordinates of the points C and D are $(2\pi/3+\Phi, 0)$ and $(\Phi+5\pi/6, 1)$ respectively.

Hence, the equation of the line CD at point C₆ can be written as (5).

$$z_6 = \left(\frac{6}{\pi}\right)\left(\theta_6 - \frac{2\pi}{3} - \phi\right) \quad (5)$$

Where z₁ & z₆ = y-axis values at points C₁ and C₆ respectively m=modulation index of voltage references R₁ and R₂

From equations (2), (3), (4) and (5), the intersection angles θ_1 and θ_6 at points C₁ and C₆ can be found out by equating the equations of voltage references and carriers and can be written as (6) and (7) respectively.

$$-m \sin \theta_1 = \left(\frac{6}{\pi}\right)(\theta_1 - \phi) \quad (6)$$

$$m \sin \theta_6 = \left(\frac{6}{\pi}\right)\left(\theta_6 - \frac{2\pi}{3} - \phi\right) \quad (7)$$

Equation (7) can be modified as (8) by putting the constraint of quarter wave symmetry (2).

$$m \sin \theta_1 = \left(\frac{6}{\pi}\right)\left(\frac{\pi}{3} - \theta_1 - \phi\right) \quad (8)$$

By adding equations (6) and (8), the value of ϕ can be found as (9).

$$\phi = \frac{\pi}{6} \text{ rad} \quad (9)$$

Equation (9) shows the condition for quarter wave symmetry among pole voltage waveforms VHB1, when p=3. For carriers having frequency p=3n (where n=1, 3, 5, 7, 9,, etc.) times the fundamental frequency and with their zero crossings lagging the fundamental voltage reference zero crossings by Φ rad, the condition for quarter wave symmetry among pole voltage waveform VHB1 can be found by using (10)-(14). Here, for quarter wave symmetry, the constraint is given by (10).

$$\theta_{6n+1-l} = \pi - \theta_l \text{ for } l = 1, 2, \dots, 3n \quad (10)$$

For l=1

$$-m \sin \theta_1 = \left(\frac{6n}{\pi}\right)(\theta_1 - \phi) \quad (11)$$

$$m \sin \theta_{6n} = \left(\frac{6n}{\pi}\right)\left\{\theta_{6n} - \phi - \frac{(3n-1)\pi}{3n}\right\} \quad (12)$$

By putting the constraint of quarter wave symmetry

$\theta_{6n} = \pi - \theta_1$ in (12), one gets

$$m \sin \theta_1 = \left(\frac{6n}{\pi}\right)\left(\frac{\pi}{3n} - \theta_1 - \phi\right) \quad (13)$$

By adding (11) and (13), the value of ϕ can be found as (14).

$$\phi = \left(\frac{1}{3n}\right)\left(\frac{\pi}{2}\right) \text{ rad} \quad (14)$$

Thus, by speaking to Φ regarding bearer period, it tends to be presumed that, for keeping up quarter wave symmetry among post voltage waveform VHB1, the zero intersection of the transporter should slack the zero intersection of the voltage reference by $\pi/2$ rad.

1. The zero intersections of the voltage references ought to concur with the zero intersections of the bearer.

2. The zero intersections of the bearer ought to be set at $\pm\pi/2$ rad with deference to the zero intersections of the voltage references (where 2π rad signifies one transporter period).

In the event that we have just two H-Bridges per stage, at that point their individual bearers ought to be stage moved from one another by $\pi/2$ rad. One transporter can fulfill condition I and the other bearer can fulfill condition II as portrayed previously. The yield voltage of both the H-Bridges will keep up quarter wave symmetry and henceforth their total will likewise keep up quarter wave symmetry. Yet, as the quantity of fell H-Bridges builds (more than two) it is beyond the realm of imagination to expect to put all the zero intersections of transporters at 0 rad or $\pm\pi/2$ rad regarding the zero intersections of voltage references, as the stage distinction between (π/x rad for $x \geq 2$ number of fell H-spans) zero intersections of the bearers diminishes. For two fell H- Scaffolds, the following area manages the conditions for keeping up quarter wave symmetry among resultant post voltage VHB, where the zero

intersections of the bearers are put other than 0rad or $\pm\pi/2$ rad regarding the voltage reference zero intersections. 2) Two Cascaded H-Bridges For two fell H-Bridges (Fig.1.(b)), it is additionally conceivable to keep up quarter wave symmetry among the resultant shaft voltage waveform VHB, regardless of individual scaffold voltage waveforms VHB1 and VHB2 not keeping up quarter wave symmetry. Two methodologies are conceivable and pointed beneath. Both the methodologies are broke down in the coming segments.

1.Zero intersections of transporters CHB1 and CHB2 are put on the two sides of the zero intersection of bearer Cref1 (where Cref1 is an invented bearer whose zero intersections are in stage with the zero intersections of the voltage references R1 and R2).

2.Zero intersections of bearers CHB1 and CHB2 are set on the two sides of the zero intersection of transporter Cref2 (where Cref2 is an imaginary bearer whose one positive or negative pinnacle shows up in a similar moment as that of the zero intersections of the voltage references R1 and R2).In different words, it very well may be expressed that Cref1 fulfills condition I and Cref2 fulfills condition II. The transporters CHB1 and CHB2 are utilized for producing entryway beats of H-Bridge1 and H-Bridge2 individually in a twofold fell H-Bridge (Fig.1.(b)). The voltage reference R1 is utilized for creating the door beats of S11, S12, S13 and S14. The entryway beats of S13, S14, S23 and S24 are produced by voltage reference R2. Fig.4 and Fig.5 demonstrate the voltage references, R1 and R2 alongside transporters CHB1 and CHB2 with deference to the imaginary bearers Cref1 and Cref2 separately.

A) Approach I

Fig.4 demonstrates the case, where the zero intersection of bearer CHB1 drives the zero intersection of imaginary transporter Cref1 by Φ_1 rad, while the zero intersection of transporter CHB2 slacks the zero intersection of Cref1 by Φ_2 rad for $p=3$. Fig.4.(b) demonstrates that the resultant shaft voltage VHB, does not keep up quarter wave evenness. So as to demonstrate the crossing point focuses and convergence edges, just the half cycle of every waveform is appeared in Fig.4. For keeping up quarter wave balance of the subsequent shaft voltage VHB, the condition to be fulfilled among voltage reference and bearer convergence guides C1 toward C12 is given by (15).

$$\theta_{13-l} = \pi - \theta_l \quad \text{for } l=1, 2, 3, 4, 5 \text{ and } 6 \quad (15)$$

For $l=1$, the values of θ_1 and θ_{12} , at points C_1 & C_{12} can be found out by equating the equations of voltage reference and carriers and can be written as (16) and (17) respectively.

$$-m \sin \theta_1 = \left(\frac{6}{\pi}\right)(\theta_1 - \phi_2) \quad (16)$$

$$-m \sin \theta_{12} = -\left(\frac{6}{\pi}\right)(\theta_{12} - \pi + \phi_1) \quad (17)$$

Equation (17) can be modified as (18) by putting the condition of quarter wave symmetry (15).

$$m \sin \theta_1 = \left(\frac{6}{\pi}\right)(\phi_1 - \theta_1) \quad (18)$$

By adding (16) and (18), the condition for quarter wave symmetry can be found out as (19).

$$\phi_1 = \phi_2 \quad (19)$$

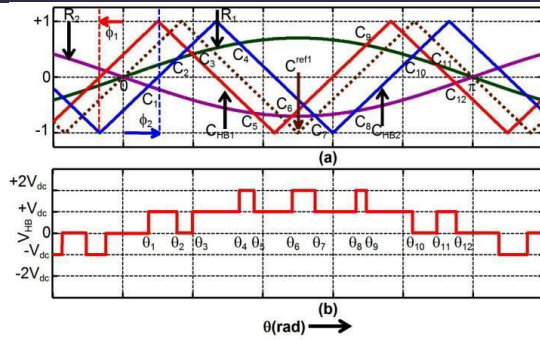


Fig. 4.(a)References R_1 and R_2 and Carriers C_{HB1} and C_{HB2} when C^{ref1} is in phase with voltage references ($p=3$); (b) V_{HB} .

From (19)

it can be observed that the quarter wave symmetry among resultant pole voltage waveform V_{HB} can be maintained if $\Phi_1 = \Phi_2$, i.e. the zero crossings of carriers C_{HB1} and C_{HB2} are placed equidistantly from the zero crossings of carrier C^{ref1} . In a similar way the conditions for quarter wave symmetry

at the other intersection points can also be derived and each pair of intersection points will result in the condition of (19). Equation (19) shows the condition for quarter wave symmetry among pole voltage waveform V_{HB} , when $p=3$. For carriers having frequency $p=3n$ (where $n=1, 2, 3, 4, 5, 6, 7, 8, 9, 10, \dots$, etc.) times the fundamental frequency and with their zero crossings lagging and leading the zero crossing of C^{ref1} by Φ_1 rad and Φ_2 rad respectively, the condition for quarter wave symmetry among resultant pole voltage waveform V_{HB} can be found by using the above approaches.

b) Approach II

In this second approach (Fig. 5), the zero crossing of carrier C_{HB1} leads the zero crossing of carrier C^{ref2} by Φ_1 rad, whereas the zero crossing of carrier C_{HB2} lags the zero crossing of carrier C^{ref2} by Φ_2 rad for $p=3$. Fig. 5.(b) shows that the resultant pole voltage V_{HB} does not maintain quarter wave symmetry. For maintaining

quarter wave symmetry among resultant pole voltage waveform V_{HB} , the condition to be satisfied among voltage reference and carrier intersection points C_1 to C_{12} is given by (20).

$$\theta_{13-l} = \pi - \theta_l \text{ for } l=1, 2, 3, 4, 5 \text{ and } 6 \quad (20)$$

For $l=1$, the values of θ_1 & θ_{12} , at points C_1 and C_{12} can be found out by equating the equations of voltage references and carriers and can be written as (21) and (22) respectively.

$$-m \sin \theta_1 = \left(\frac{6}{\pi} \right) \left(\theta_1 - \frac{\pi}{6} + \phi_1 \right) \quad (21)$$

$$m \sin \theta_{12} = \left(\frac{6}{\pi} \right) \left(\theta_{12} - \frac{5\pi}{6} - \phi_2 \right) \quad (22)$$

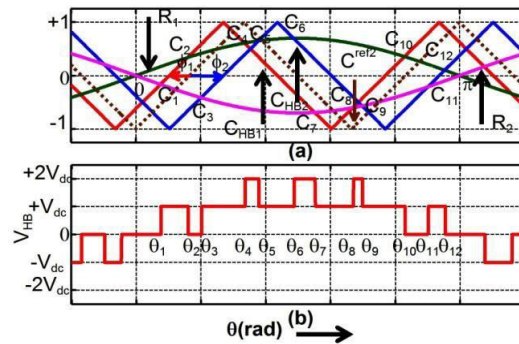


Fig. 5.(a) References R_1 and R_2 and Carriers C_{HB1} and C_{HB2} when C^{ref2} is $\pi/2$ rad lagging with respect to voltage references ($p=3$); (b) V_{HB} .

By putting the condition of quarter wave symmetry (20) in (22) can be simplified as (23).

$$m \sin \theta_1 = \left(\frac{6}{\pi} \right) \left(\frac{\pi}{6} - \theta_1 - \phi_2 \right) \quad (23)$$

By adding (21) & (23), the condition for quarter wave symmetry can be found out as (24).

$$\phi_1 = \phi_2 \quad (24)$$

From (24) it can be observed that, here also the quarter wave symmetry among resultant pole voltage waveform V_{HB} can be maintained if $\Phi_1 = \Phi_2$, i.e. the zero crossings of carriers C_{HB1} and C_{HB2} are equidistant from the zero crossing

of carrier C^{ref2} . The condition of quarter wave symmetry can also be shown as $\Phi_1 = \Phi_2$, if the zero crossing of carrier C^{ref2} lead $\pi/2$ rad from the zero crossing of the voltage references. Same condition for quarter wave symmetry can be derived for carriers having frequency $p = 3n$ (where $n = 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, \dots$, etc.) times the fundamental frequency.

c) Conditions for quarter wave symmetry with $x (\geq 2)$ numbers of cascaded H-Bridges TABLE-I

Three Cascaded H-Bridges

Position of C_{HB1}	Position of C_{HB2}	Position of C_{HB3}
$\mp \pi/3$ rad	0 rad	$\pm \pi/3$ rad
$\mp \pi/6$ rad	$\pm \pi/6$ rad	$\pm \pi/2$ rad
$\mp \pi/6$ rad	$\mp \pi/2$ rad	$\mp 5\pi/6$ rad
0 rad	$\mp \pi/3$ rad	$\mp 2\pi/3$ rad

Zero crossing of voltage reference is placed at 0 rad

From the past exchanges on quarter wave balance, it very well may be broadened that there can be numerous methodologies of keeping up quarter wave evenness among the resultant post voltage waveform. For example in the event that there are two Hbridges, at that point one methodology can be to put one transporter concurring with C^{ref1} and the other bearer harmonizing with C^{ref2} . Another methodology can be to put two bearers with the end goal that one transporter drives C^{ref1} (or C^{ref2}) by $\pi/4$ rad and the other transporter slacks C^{ref1} (or C^{ref2}) by $\pi/4$ rad. On the off chance that there are three fell H-Bridges, at that point the potential methodologies are given as a table in TABLE-I. Subsequently, it is essential to decide the general way of thinking of putting the transporters as for the voltage reference to guarantee quarter wave evenness for a $(2x+1)$ level fell H-Bridge staggered inverter (x number of fell

H-Bridges). The way of thinking is diagnostically clarified in this area. For this examination, position of a bearer is characterized by the situation of the zero intersection of the transporter.

Case:-I (With one carrier being at 0^{th} position (0^{th} carrier), k numbers of carriers are present on the left hand side (between points A and B) and $(x-k-1)$ numbers of carriers are present on the right hand side (between points D & H) of the 0^{th} carrier). It is to be noted that the zero crossing of voltage reference R coincides with the zero crossing of the carrier placed at 0^{th} position.

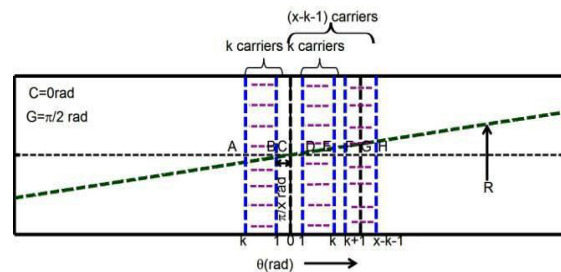


Fig. 6. Positive zero intersection (point C) of standardized voltage reference R harmonizes with the positive zero intersection (point C) of 0^{th} transporter.

Fig.6 demonstrates the case:- I, where k quantities of bearers are available on the left hand side of 0^{th} transporter and $(x-k-1)$ quantities of transporters are available on the correct hand side of 0^{th} bearer. The zero intersections every bearer isolated from one another by π/x rad. The positive zero intersection of the standardized voltage reference R concurs with the positive zero intersection of 0^{th} bearer. It can likewise be seen from Fig.6 that, equivalent numbers of bearers are available around point C between focuses A-B & D-E. Henceforth, the resultant yield shaft voltage waveform of the H-Bridges utilizing these transporters keeps up quarter wave

symmetry. Additionally, the post voltage yield of the H-Bridge utilizing 0th transporter keeps up quarterwave symmetry. Subsequently, keeping up quarterwave symmetry among the resultant post voltage waveform of x quantities of fell H-Bridges it ought to be demonstrated that the positive zero intersections of transporters bearers between focuses F-H) are available equidistantly from $\pi/2$ rad (point G). The removes between focuses G-F and H-G are determined as (25) and (26).

$$FG = \frac{\pi}{2} - (k+1) \left(\frac{\pi}{x} \right) = \left(\frac{x-2k-2}{2} \right) \left(\frac{\pi}{x} \right) \quad (25)$$

$$GH = (x-k-1) \left(\frac{\pi}{x} \right) - \frac{\pi}{2} = \left(\frac{x-2k-2}{2} \right) \left(\frac{\pi}{x} \right) \quad (26)$$

Equations (25) & (26) shows that the positive zero crossings of $(k+1)^{th}$ and $(x-k-1)^{th}$ carriers are placed equidistantly from $\pi/2$ rad.

The above explanation can be extended to even/odd numbers of cascaded H-bridges. For an even number of cascaded H-Bridges one carrier is present at $\pi/2$ rad and equal numbers of carriers are present on both sides of $\pi/2$ rad. For an odd number of cascaded H-Bridges no carrier is present at $\pi/2$ rad and equal numbers of carriers are present on both sides of $\pi/2$ rad.

Case:-II (k numbers of carriers are present on the left hand side (between points A and B) and (x-k) numbers of carriers are present on the right hand side (between points D and H) of the point C (the mid-point of points B and D) and the positive zero crossing of voltage reference R coincides with point C). There is no 0th carrier in this case.

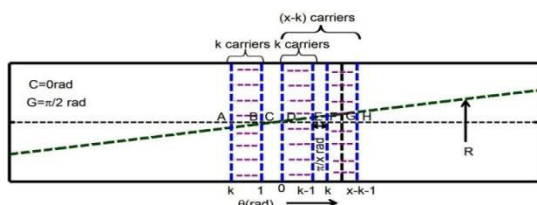


Fig. 7. Positive zero intersection (point C) of standardized shaft voltage reference R is set

in the middle of the positive zero intersections (focus Band Do two adjoining bearer sets). Fig. 7 demonstrates the case: II, where k quantities of transporters are available on the left hand side of point C and (x-k) quantities of bearers are available on the right hand side of point C. The zero intersections of every bearer are isolated from one another by π/x rad. The positive zero intersection of the standardized voltage reference R corresponds with the point C (mid-purpose of the two contiguous bearers). It likewise can be seen from Fig. 7 that, equivalent numbers of transporters are available around point C between focuses A-B and D-E. Consequently, the resultant yield post voltage waveform of the H-Bridges utilizing these transporters keeps up quarterwave symmetry. For keeping up quarterwave symmetry among the resultant post voltage waveform of x quantities of fell H-Bridges it ought to be demonstrated that the positive zero crossings of k^{th} and $(x-k)^{th}$ carriers (among n-2k carriers between points F & H) are present equidistantly from $\pi/2$ rad (point G).

The distances between points G-F and H-G are calculated as (27) and (28).

$$FG = \frac{\pi}{2} - \left\{ k \left(\frac{\pi}{x} \right) + \frac{\pi}{2x} \right\} = \left(\frac{x-2k-1}{2} \right) \left(\frac{\pi}{x} \right) \quad (27)$$

$$GH = \left\{ (x-k-1) \left(\frac{\pi}{x} \right) + \frac{\pi}{2x} \right\} - \frac{\pi}{2} = \left(\frac{x-2k-1}{2} \right) \left(\frac{\pi}{x} \right) \quad (28)$$

Equations (27) & (28) show that the positive zero crossings of k^{th} and $(x-k)^{th}$ carriers are placed equidistantly from $\pi/2$ rad. The above clarification can be stretched out to even/odd quantities of fell H-spans. For a considerably number of fell H-Bridges no transporter is available at $\pi/2$ rad and equivalent quantities of bearers are available

on the two sides of $\pi/2$ rad. For an odd number of full H-Bridges one transporter is available at $\pi/2$ rad & equivalent quantities of bearers are available on the two sides of $\pi/2$ rad.

d) Generalized Condition for maintaining Quarter Wave Symmetry

TABLE-II

x Cascaded H-Bridge Multilevel Inverters. (There are x phase shifted carriers with π/x phase difference between any two adjacent carriers)

CONDITION FOR QUARTER WAVE SYMMETRY $f_c/f_s=3n$ (where $n=1,2,3,4,\dots$ etc.)	
1	Zero crossing of voltage reference coincides with zero crossing of any carrier
2	Zero crossing of voltage reference is placed at the mid-point between positive (or negative) zero crossings of any two adjacent carriers

TABLE-II abridges the conditions to keep up quarter wave symmetry among resultant post voltage waveform for x ($x \geq 2$) quantities of full H-Bridge inverter. It is to be noticed that, it is required to fulfill any one condition referenced in Table II so as to keep up quarter wave symmetry. It is seen that in the event that any of these conditions is kept up, at that point forgetting about the in-stage and $\pi/2$ rad-stage bearers (on the off chance that they exist), equivalent number of transporters will be set on the two sides of C_{ref1} (as appeared in Fig. 4) or C_{ref2} (as appeared in Fig. 5) or both. e) Power appropriation between every cell

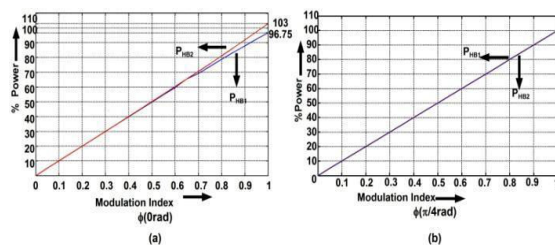


Fig.8 Power appropriation between every H-Bridge of a five-level CHBMLI (a) Positive zero intersection of CHB1 corresponds with the zero intersections of R1 and R2 and (b) Zero intersections of R1 and R2 are set at $\pm\pi/4$ rad with reference to the zero intersections of CHB1 & CHB2 for $p=3$.

For PSPWM system with a high exchanging recurrence, every H-Bridge handles equivalent power. Yet, when the proportion p turns out to be low, inconsistent power dissemination happens between the full H-Bridges. The reason is, same current goes through every module. In any case, their immediate yield voltage waveforms are unique. Subsequently, the immediate power incorporated over a total principal cycle may result in various qualities. Fig. 8 demonstrates that, when the zero intersections of the voltage references match with the zero intersection of one of the bearers, state of inconsistent conveyance of intensity between the H-Bridges happens. However, when the zero intersections of the voltage references are put equidistantly between the zero intersections of the two bearers, at that point every H-Bridge offers equivalent power. It is to be noticed this is only a particular model. In general x CHBMLIs inconsistent power conveyance may occur for lesser estimation of p .

f) Fundamental displacement and harmonic distortion without maintaining QWS

In order to study the importance of maintaining QWS, with $p=3$, the angle Φ (distance between the zero crossing of voltage reference and the positive zero crossing of C_{HB1}) is varied in H-Bridge 1 (Fig. 1.(b)) from 0 rad to $\pi/6$ rad (0 rad to $\pi/2$ rad in terms of carrier period). Hence, the positive zero crossing of C_{HB2} varies from $\pi/6$ rad to $\pi/3$ rad with respect to the fundamental reference ($\pi/2$ rad to π rad in terms of carrier period). Fig. 9.(a) shows that, for a single H-bridge (Hbridge1), maximum phase displacement (α_{HB1}) occurs between the voltage reference and the fundamental of pole voltage for $\Phi=\pi/4$ rad (in terms of carrier period). At both the

extreme ends, i.e. at $\Phi=0\text{rad}$ (Condition of QWS) and $\Phi=\pi/2\text{rad}$ (Condition of QWS) the phase displacements are zero. Similarly, the Fig.9.(b) shows at $\Phi=0\text{rad}$ (Condition of QWS), $\Phi=\pi/4\text{rad}$ (Condition of QWS) and $\Phi=\pi/2\text{rad}$ (Condition of QWS) the phase displacements are zero for the double cascaded H-Bridges. Fig.9 also shows that the phase displacement for a single HB is higher comparing to the double cascaded HBs. If the number of cascaded H-bridges is further increased, then the phase displacement reduces to lesser values and hence, for a very large number of cascaded H-bridge cells, with phase shifted carriers, phase displacement of the resultant pole voltage is negligible even without perfect quarter wave symmetry.

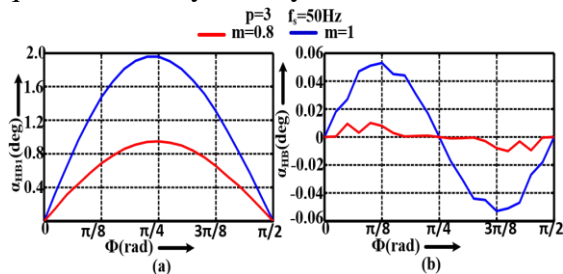


Fig. 9. Fundamental voltage displacements of (a) Single H-Bridge and (b) Double cascaded H-Bridges for $p=3$, $f_s=50\text{Hz}$ and with modulation indexes of 0.8 and 1.

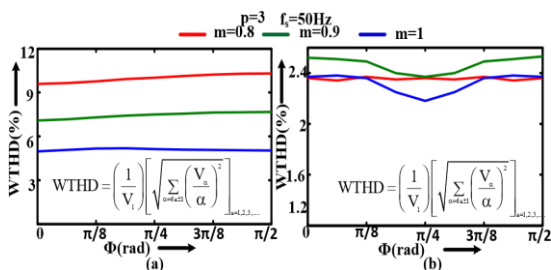


Fig. 10. WTHD (line voltage V_{RY}) variations w.r.t

the variation of Φ for higher modulation indexes (0.8 to 1) for (a) single H-Bridge and (b) double cascaded H-Bridges with $p=3$, $f_s=50\text{Hz}$. In order to check the variation of the harmonic profile of the line voltage with respect to the variation of Φ (distance between the zero crossing of voltage

reference and the positive zero crossing of C_{HB1}), the WTHD plots for line voltage V_{RY} , are plotted in Fig.10 for a three level (single H-bridge) and five level (two cascaded H-bridges) CHBMLI, for three modulation indexes 0.8, 0.9 and 1, with $p=3$ and $f_s=50\text{Hz}$. For single H-bridge, the positions $\Phi = 0\text{rad}$ and $\pi/2\text{rad}$ are the conditions for quarter wave symmetry whereas for two cascaded H-bridges, the positions 0rad , $\pi/4\text{rad}$ and $\pi/2\text{rad}$ are the states of keeping up QWS among shaft voltage waveform. Here, it tends to be seen that for a solitary H-connect, WTHD fluctuates nearly in a level profile. For two fell Hbridges, for higher adjustment files, least WTHD happens at $\Phi=\pi/4\text{rad}$ (for example the zero intersection of voltage reference is put at the midpoint of the zero intersections of two bearers) which is a legitimate condition for quarter wave symmetry. The improvement is obviously unreasonably little for thought in a handy circumstance. The proposed PSPWM system does not give a superior symphonious voltage profile contrasted with the strategies proposed in [8][20][21] as the door heartbeats are created with no consonant minimization method. References [8] & [21] demonstrate the utilizations of SOPWM method, where the exchanging moments are determined for limiting certain voltage music alongside minimization of current mutilation. Reference [20] computes the exchanging moments by limiting the required voltage sounds alongside the limitation of limiting the throbbing torque. The proposed PSPWM method does not profess to give diminished current or torque swell. Be that as it may, it tends to be expressed that with lower p and decreased number of H-Bridges the proposed PSPWM procedure gives better execution contrasted with the conventional PSPWM method (the

zero intersections of voltage references can be put self-assertively regardless of zero intersections of the stage moved transporters). Further, as the proposed procedure is a transporter based plan, synchronization in a profoundly powerful circumstance like field arranged control can be accomplished with a lot lesser computational unpredictability contrasted with the plans of [8], [20], [21] and so on. Be that as it may, with the expansion in the quantity of HBridges and p the consonant profile is practically like a customary PSPWM system [22]-[25].

III. EXPERIMENTAL RESULTS

The synchronization methodology for the CHBMLIs is checked with three stage squirrel confine enlistment engine drive parameters are given in APPENDIX worked in open circle V/f mode, which is provided from a five level three stage CHBMLI research facility model. The dc-interface voltage V_{dc} of every H-Bridge is kept up at 100V during the trial to work the engine at half of the appraised voltage. Both the conditions for keeping up quarter wave symmetry in the resultant shaft voltage designs VHB, as talked about in Section II, are tried tentatively. For exploratory

confirmation, the proportion p is maintained at 3 in control to demonstrate the shaft voltage heartbeat designs unmistakably. Fig.11.(a) demonstrates that the zero intersections of bearer CHB1 harmonizes with the zero intersections of voltage references $R1$ and $R2$. Fig.11.(b) demonstrates that the individual extension voltage waveforms V_{HB1} and V_{HB2} alongside the resultant post voltage waveform V_{HB} keep up half wave symmetry and quarter wave symmetry. Fig.11.(d) demonstrates that the zero intersections of voltage references $R1$ and $R2$ are put equidistantly in the middle of the zero intersections of bearer CHB1 and CHB2. Fig.11.(e) demonstrates the individual scaffold voltages V_{HB1} and V_{HB2} and resultant post voltage V_{HB} . It very well may be seen that the individual extension voltages keep up half wave symmetry, however not quarter wave symmetry, though resultant shaft voltage waveform keeps up half wave symmetry and quarter wave symmetry. Fig.11.(c) and (f) demonstrate that the three stage symmetry is kept up among three stage shaft voltage waveforms.

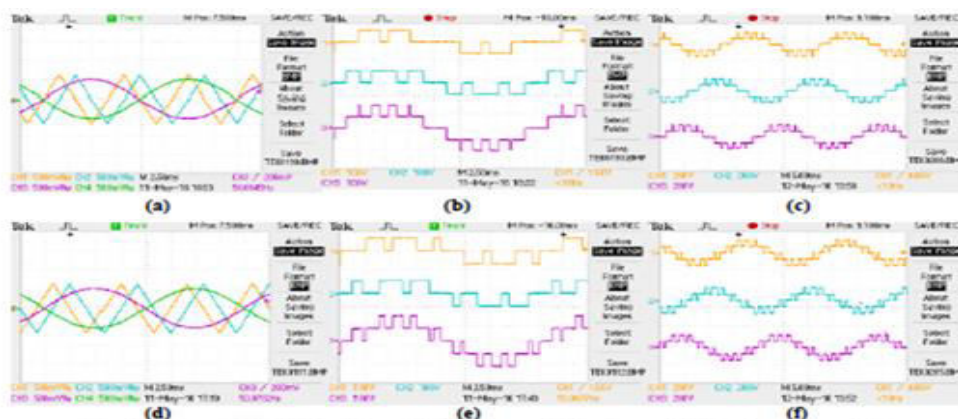


Fig. 11.(a) and (d) Ch.1:-CHB₁, Ch.2:-CHB₂, Ch.3:-R₁ and Ch.4:-R₂; (b) and (e) Ch.1:-V_{HB1}, Ch.2:-V_{HB2} and Ch.3:-V_{HB}} and (c) and (f) Ch.1:-V_{RO}, Ch.2:-V_{BO} and Ch.3:-V_{YO} when (i) the zero crossings of voltage references are in phase with the zero crossings of carrier CHB₁ and (ii) the zero crossings of voltage references are placed at the midpoint of the positive zero crossings of carriers CHB₁ & CHB₂ for $f_c = 3f_s$ with a modulation index of 0.8 and $f_s = 50\text{Hz}$.

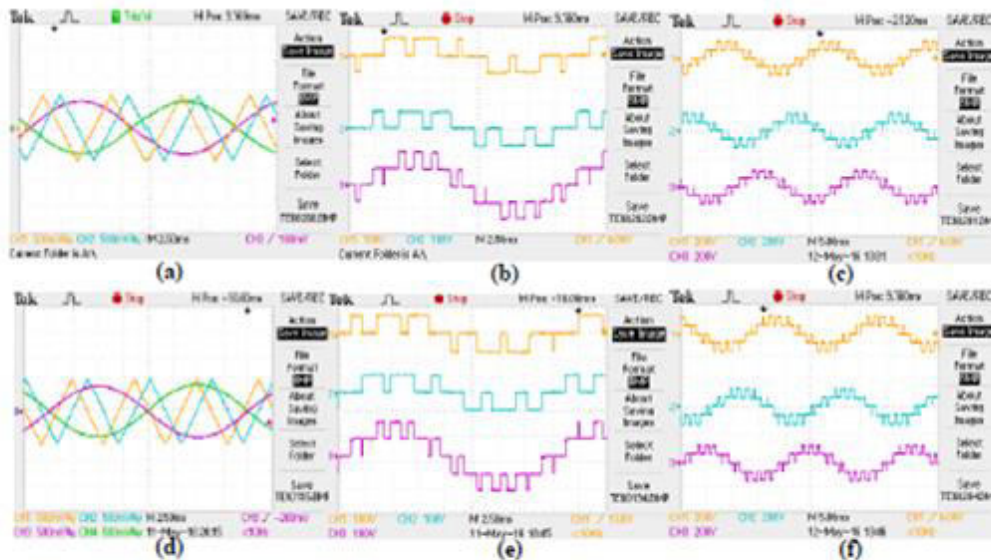


Fig. 12.(a) and (d) Ch.1:- C_{HB1} , Ch.2:- C_{HB2} , Ch.3:- R_1 and Ch.4:- R_2 ; (b) and (e) Ch.1:- V_{HB1} , Ch.2:- V_{HB2} and Ch.3:- V_{HB} and (c) and (f) Ch.1:- V_{RO} , Ch.2:- V_{RQ} and Ch.3:- V_{YO} when (i) the zero crossings of voltage references are placed at $+\pi/12$ rad with respect to the zero crossings of carrier C_{HB1} for $f_c=3f_r$ with a modulation index of 0.8 and $f_r=50$ Hz and (ii) for $f_c=160$ Hz with a modulation index of 0.8 and $f_r=50$ Hz.

So as look at the proposed PSPWM system, the analysis is finished with two arrangements of bearers CHB1 & CHB2 where the zero intersections of voltage references, R1 & R2 are put at an edge of $+\pi/12$ rad with deference to the zero intersection of CHB1. The proportion p is kept at 3. This condition guarantees the three stage symmetry and half wave symmetry. In any case, quarter wave symmetry is not kept up. Fig. 12.(a) demonstrates the previously mentioning conditions for voltage references, R1 & R2 and bearers CHB1 and CHB2. Fig. 12.(b) demonstrates that the individual scaffold voltages V_{HB1} and V_{HB2} alongside resultant extension voltage V_{HB} keep up half wave symmetry without keeping up quarter wave symmetry. Fig. 12.(c) demonstrates that the three stage shaft voltages keep up three stage symmetry. So also, a couple of transporters CHB1 and CHB2 having recurrence of 160Hz offbeat PWM are utilized to produce similar waveforms. The outcomes are appeared in Fig. 12.(d). It tends to be seen that the individual scaffold voltages

alongside the resultant extension voltage don't keep up half wave symmetry, quarter wave symmetry or three stage symmetry. During the examination, the regulation file of the voltage references is kept at 0.8 with a recurrence of 50Hz. The symphonious ranges of line voltages (VRY) with & without waveform symmetries and synchronization are analyzed in Fig. 13 Fig. 14. Fig. 13.(b) and demonstrate symphonious range of line voltage VRY which is the aftereffect of keeping up quarter wave symmetry for one of the conditions referenced in TABLE II. Fig. 14.(b) demonstrates the consonant range of the line voltage VRY, when there is no QWS present among the post voltage waveform. The consonant ranges of from even request voltage music, as the post voltage waveform looks after HWS. The consonant ranges of Fig. 13.(b) Fig. 13.(d) in Fig. 14.(b) demonstrate that the line voltage is free from triplen request voltage music. Henceforth, the three stage symmetry is kept up among the three stage shaft voltage waveforms. The WTHD of line voltage VRY for the over three cases are

practically equivalent. The minor contrast is now appeared in Fig.10. Fig.14.(d) demonstrates that the consonant range of the line voltage VRY contains a little dc balance for 160Hz stage moved bearers. This DC balance is seen in the symphonious range, as the consonant ranges are plotted for one central cycle. For an offbeat bearer, the post voltage waveform alongside the line voltage waveform are not intermittent thinking of one as central time of the voltage reference. Be that as it may, if the symphonious ranges are plotted dependent on an information of a timespan in which both the crucial and the transporters complete whole number of cycles, at that point this DC balance won't be seen in the consonant ranges of post voltage just as line voltage. Or maybe, it will show up as subharmonic segment. The subharmonic recurrence is awful for drive's application, since it might coordinate with the mechanical reverberation recurrence of the driveshaft. Additionally, it is a typical practice to utilize inverter yield channels for drives with long separation between the converter and the engine. It very well may be seen that the recurrence range with synchronized bearer are of concentrated nature, while, with nonconcurrent transporter are of disseminated nature. Consequently, structure of yield channel is increasingly affordable for synchronized transporter based methodology where the proportion between the bearer recurrence and the central recurrence (p) is exceptionally low. Additionally, nonconcurrent bearers request voltage sounds are seen in the line voltage waveform, because of the nonappearance of HWS among post voltage waveform. Some test results are additionally taken for a three stage five level CHBMLI encouraged squirrel confine enlistment engine drive

with $p=6$ and $f_s=50$ Hz. This analysis is done so as to demonstrate that the proposed PSPWM strategy is likewise substantial for bearers having a recurrence proportion p being even. Fig.15.(a) and Fig.16.(a) demonstrate the individual scaffold voltages alongside the resultant extension voltage of a solitary stage five level CHBMLI with the conditions for keeping up QWS among shaft voltage VHB (as referenced in TABLE-II). In Fig.15.(a) demonstrates that the individual extension voltages alongside the resultant scaffold voltage keep up HWS & QWS, when the zero intersection of one bearer agrees with the zero intersection of voltage references. Fig.16.(a) demonstrates the individual extension voltages don't keep up QWS however the resultant scaffold voltage keeps up QWS when the zero intersection of the voltage references are set at the midpoint of the zero intersections of the bearers. In Fig.15, Fig.16 likewise demonstrate the FFT ranges of post voltage VRO and line voltage VRY for $p=6$ and $f_s=50$ Hz with a balance record of 0.8. The consonant ranges of shaft voltage VRO in Fig.15.(b) and Fig.16.(b) demonstrate that even request voltage music are missing. It implies the post voltage waveform VRO looks after HWS. The consonant ranges of line voltage VRY in Fig.15.(d) & Fig.16.(d) demonstrate the triplen music are missing from the line voltage waveform VRY. This demonstrates the three stage shaft voltage waveforms keep up 3 stage symmetry. The THD of all waveforms are determined and WTHD is additionally appeared for line voltage VRY. The WTHD of the line voltage VRY demonstrates that the impact of lower request voltage music is indistinguishable in both the conditions referenced in TABLE-II. This occurs likewise with higher p , the WTHDs of line voltages are practically

indistinguishable independent of the various conditions for looking after QWS

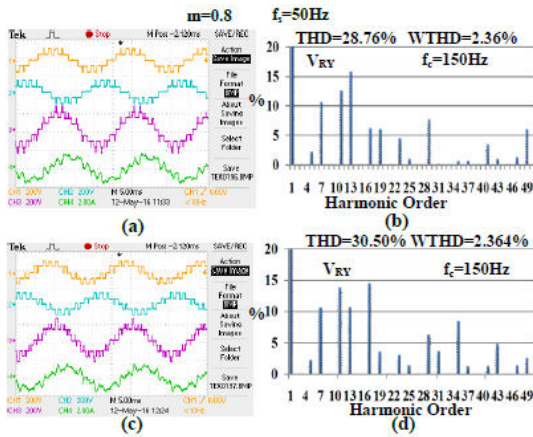


Fig. 13. (a) and (c) Ch.1:- VRO, Ch.2:- VYO, Ch.3:- VRY and Ch.4:- iR; (b) and (d) Harmonic range of VRY for (i) the zero intersections of voltage references are in stage with the zero intersections of bearer CHB1 and (ii) the zero intersections of voltage references are set at the midpoint of the positive zero intersections of transporters CHB1 & CHB2 for $f_c=3f_s$ with a tweak record of 0.8 and $f_s=50Hz$.

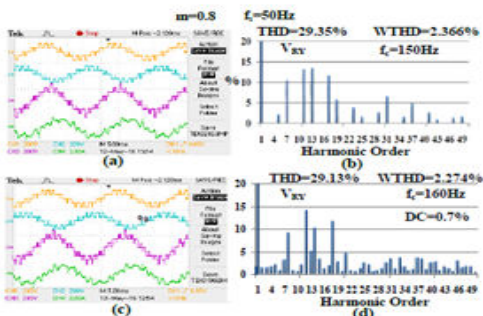


Fig. 14. (a) and (c) Ch.1:- VRO, Ch.2:- VYO, Ch.3:- VRY and Ch.4:- iR; (b) and (d) Harmonic spectrum of VRY for (i) the zero crossings of voltage references are placed at $+\pi/12$ rad with respect to the zero crossings of carrier CHB1 for $f_c=3f_s$ and (ii) $f_c=160Hz$ with a modulation index of 0.8 and $f_s=50Hz$.

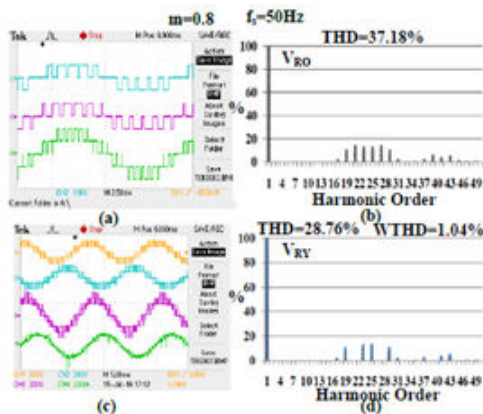


Fig. 15. (a) Ch.2:- VHB1, Ch.3:- VHB2 and Ch.4:- VHB; (b) Harmonic spectrum of VRO; (c) Ch.1:- VRO, Ch.2:- VYO, Ch.3:- VRY and Ch.4:- iR and (d) Harmonic spectrum of VRY when the zero crossings of voltage references are in phase with the zero crossings of carrier CHB1 for $f_c=6f_s$ with a modulation index of 0.8 and $f_s=50Hz$.

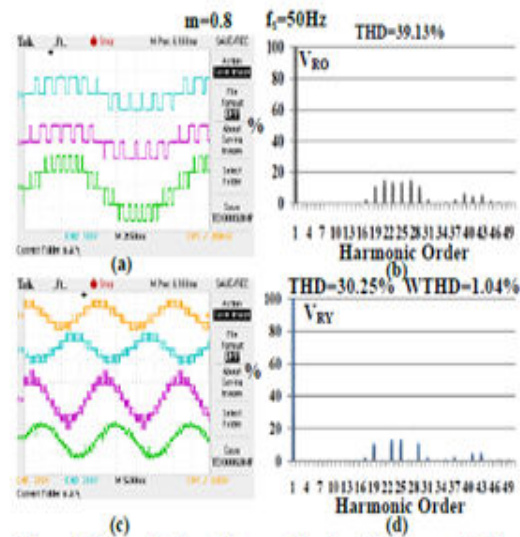


Fig. 16. (a) Ch.2:- VHB1, Ch.3:- VHB2 and Ch.4:- VHB; (b) Harmonic spectrums of VRO; (c) Ch.1:- VRO, Ch.2:- VYO, Ch.3:- VRY and Ch.4:- iR and (d) Harmonic spectrums of VRY when the zero crossings of voltage references are placed at the midpoint of the zero crossings of carriers CHB1 & CHB2 for $f_c=6f_s$ with a modulation index of 0.8 and $f_s=50Hz$.

In Fig.17 Fig.18 are taken with the assistance of a three stage five level CHBMLI nourished squirrel confining enlistment engine drive. Fig.17 likewise demonstrates the outcomes keeping up shaft voltage waveform for twofold fell H Bridges with $p=9$. Henceforth, the conditions organized in TABLE II, are legitimate for any $3n$ (where $n=1, 2, 3, \dots$ and so on.) transporter. Fig.18.(b) demonstrates the progress of the R-Phase current i_R during the moving between the synchronous transporters having $p=9$ to $p=3$. Fig.18.(b) demonstrates a smooth progress between the bearers having $p=9$ to $p=3$, as no critical homeless people are seen in engine current i_R . The consequences of the positive zero intersections of bearers CHB1 and CHB2. This methodology chosen exploratory confirmation as the power taken care of by each extension is equivalent for lower and higher estimations of adjustment files as talked about in segment II.

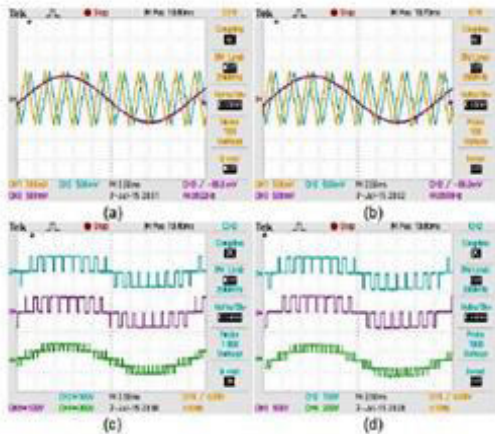


Fig. 17. (a) and (b) Ch.1:- C_{HE1} , Ch.2:- C_{HE2} , Ch.3:- R_2 and Ch.4:- R_2 and (c) and (d) Ch.1:- V_{HE1} , Ch.2:- V_{HE2} and Ch.3:- V_{HE} when (i) the zero crossings of voltage references are placed at the midpoint of the positive zero crossings of carriers C_{HE1} & C_{HE2} and (ii) the zero crossings of voltage references are in phase with the zero crossings of carrier C_{HE2} for $f_c=9f$, with a modulation index of 0.9 and $f_s=45Hz$.

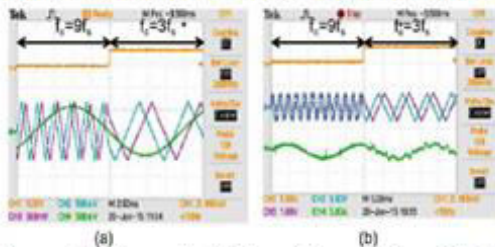


Fig. 18. (a) Ch.1:-Transition signal, Ch.2:- C_{HE1} , Ch.3:- C_{HE2} and Ch.4:-R-Phase voltage reference and (b) Ch.1:-Transition signal, Ch.2:- C_{HE1} , Ch.3:- C_{HE2} and Ch.4:- i_R during the transition from $p=9$ to $p=3$.

So as to look at the proposed PSPWM procedure for high number of level H-Bridge the test is additionally performed with an in level H-Bridge staggered inverter. Fig. 19 demonstrates the outcomes for two potential mixes of arrangement of zero intersections of voltage references with reference to the positive zero intersections of the bearers. Fig. 19.(a)(b) demonstrate the waveforms of individual scaffold voltages alongside the resultant extension post voltage separately, when the zero intersection of voltage reference corresponds with the positive zero intersection of one of the bearers. Fig. 19.(d) and (e) demonstrate the waveforms of individual extension voltages alongside the resultant scaffold

shaft voltages separately, when the zero intersection of voltage references is put at the midpoint of the positive zero intersections of two neighboring transporters. The consonant ranges of post voltage waveform VRO are appeared in Fig. 19.(c)(f) individually. The even request sounds wiped out from the symphonious range of the post voltage waveform, as every one of the waveforms look after HWS. It tends to be expressed that, with the expansion in the quantity of voltage levels, the symphonious range is moved to higher requests and their extent likewise descends. Every one of the plots are accomplished for a regulation list (m) of 0.8 at a post voltage reference recurrence (fs) of 50Hz and $p=3$.

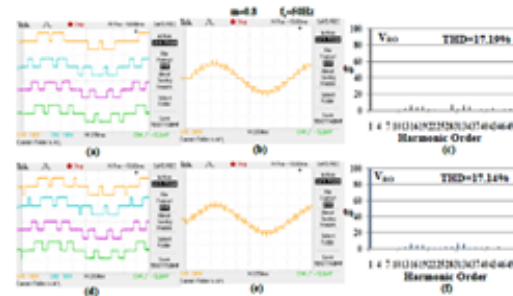


Fig. 19. (a) and (d) Ch.1:- V_{HE1} , Ch.2:- V_{HE2} , Ch.3:- V_{HE3} and Ch.4:- V_{HE4} ; (b) and (e) Ch.1:- V_{HE} and (c) and (f) Harmonic spectrum of V_{HE} when (i) the positive zero crossing of one carrier co-incides with the zero crossing of fundamental voltage reference and (ii) the zero crossing of fundamental voltage reference is placed at the midpoint of two adjacent carriers with a modulation index of 0.8, $f_s=50Hz$ and $p=3$ for a single phase nine level CHBMLI.

IV. CONCLUSION

This paper exhibits legitimately the potential spots of zero crossing points of the bearers with respect to the zero convergences of voltage references for the CHBMLIs using the PSPWM methodology for keeping up three phase symmetry, half wave symmetry and quarter wave symmetry. Three phase and half wave symmetries are kept up among the H-Bridge shaft voltage waveforms for any circumstance of zero crossing point of carrier with reference to the zero

convergence of the voltage references, as long as transporter repeat is 3n time the pivotal repeat with n being any entire number (even/odd). In any case, the spots zero crossing points of the transporters with reverence to the 0-convergences of voltage references are huge for keeping up quarterwave symmetry among the post voltage waveforms. This is consistently perused in this article for single and two level H-Bridges and summed up for x number of level H-Bridges. The assessments likely checked with the help of a three phase five level CHBMLI investigate office model and the results are shown.

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