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MINIMIZED EQUIPPED CAPACITOR AND ITS APPLICATION TO STATIC VAR COMPENSATOR WITH FULL-BRIDGE REACTIVE POWER COMPENSATOR

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Abstract— these project converses common understanding between the “VSC-based” and the “capacitor-based” reactive power compensators, and intends a modulation-controlled full-bridge reactive power compensator with condensed and optimized capacitance. The projected compensator has advantages from both types, low-harmonics characteristics by the modulation, which comes from the VSC-based compensator, and little necessary capacitance, which comes from the capacitor-based compensator. The power demand is reduced either by installing new generating stations, or by extending the range of the existing load ability limits. As a result, the existing transmission lines are heavily loaded than ever before and one impact of this is the threat of reducing stability. So Voltage instability is a major problem which attracting worldwide interest because of its result of voltage collapses. In earlier, power system stabilizers (PSS) are used for improving small signal stability. FACTS controllers are currently being incorporated with the power system for dynamic performances. In this project, STATCOM is proposed for improving voltage stability.

Keywords: Voltage Source Converter (VSC), Reactive Power Compensator. Static Synchronous Compensator(STATCOM),

INTRODUCTION

STATCOM based on voltage source converter (VSC) can be said to be the most advanced shunt-type reactive power compensator. In contrast with thyristor-based reactive power compensators, the STATCOM can directly generates reactive power and has good harmonics characteristics due to the use of full-controlled semi-conductor switches. A series compensation is also studied for the ac power trans-mission and distribution. This work concentrates on reactive power compensators beginning the point of stored energy in the capacitor banks. By applying this thought to the shunt-type static var compensator, a static synchronous compensator can be accomplished with reduced-sized capacitor. The theory can be applied to shunt applications. A shunt-type full-bridge reactive

power compensator with line-frequency switching and condensed capacitance has been projected in [9]. It has advantages of line-frequency switching and small-sized capacitor; nevertheless, large-harmonic current generated and large-grid-connected inductors wanted are challenges. Disadvantages of the line-frequency switching have an important impact in shunt-type compensator; consequently, this project applies the theory to the shunt-type reactive power compensator. The resultant shunt reactive power compensator has little capacitance compared to the normal voltage-source-type single-phase STATCOM, and accomplishes low-harmonic current and compacted inductor as equal as for the typical STATCOM.

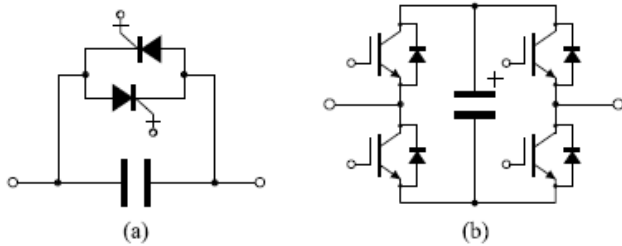


Fig. 1.1 Circuit configurations of two types of series compensators (a) GCSC (b) Voltage-source-type full-bridge compensator including SSSC and MERS

STATIC SYNCHRONOUS COMPENSATORS (STATCOM)

Line commutating thyristor device-based solid-state reactive power compensators were developed in the 1970s. These are used either as thyristor switched capacitors or thyristor controlled reactor (TCRs) or a combination thereof with passive filters eliminating dominant harmonics generated from electronic switching phenomenon. These are basically VAR impedance-type controllers, commonly known as static VAR compensator (SVC), where susceptance of the TCR is controlled by varying the firing angle. The technology is well matured, but its operational flexibility and versatile applications are limited. With the advent of voltage-source converter (VSC) technology built upon self-commutating controllable solidstate switches viz. gate turn-off thyristor (GTO), insulated gate bipolar transistor (IGBT), injection-enhanced gate transistor (IEGT), integrated gate commutated thyristor (IGCT) or gate commutated thyristor (GCT) and so on, it has ushered a new family of FACTS controllers such as static synchronous compensators (STATCOM) and unified power flow controller (UPFC) have been developed. The self-commutating VSC, called as DC-to-AC converter, is the backbone of these controllers being employed to regulate reactive current by generation and absorption of controllable reactive power with various solid-state switching techniques. The major attributes of

STATCOM are quick response time, less space requirement, optimum voltage platform, higher operational flexibility and excellent dynamic characteristics under various operating conditions. These controllers are also known as STATic COMPensator (STATCOM), advanced static VAR compensator (ASVC), advanced static VAR generator (ASVG), STATic CONDenser (STATCON), static var generator (SVG), synchronous solid-state VAR compensator (SSVC), VSC-based SVC or self-commutated SVC or static synchronous compensator (SSC or S2C). EPRI in USA is a pioneer to conduct research in this area and has been instrumental to develop a number of existing STATCOM projects in collaboration with power utilities/industries. Power industries such as GE, Siemens, ABB, Alstom, Mitsubishi, Toshiba and so on, with their in-house R&D facilities have given birth to many versatile STATCOM projects presently in operation in high-voltage transmission system to control system dynamics under stressed conditions. The VSC-based STATCOM has emerged as a qualitatively superior technology relative to that of the line commutating thyristor-based SVC being used as dynamic shunt compensator. GTO-based VSCs (GTO-VSC), commercially available with high power capacity, are employed in high power rating controllers with triggering once per cycle [fundamental frequency switching (FFS)]. Although IGBT and IGCT devices are available with reasonably good power ratings, these are being mainly used in low-to medium rating compensators operated under pulse-width modulation (PWM) switching, that is, multiple switching (1–3 kHz) in a cycle of operation. Use of these switching devices in high power rating controllers is yet to be fully commercialized and therefore its use is limited. In the state-of-the-art STATCOM equipments, two major topologies of VSC-bridges viz. multi-pulse and multi-level are the most common for operation under FFS or PWM mode or selective harmonic elimination modulation. For high power

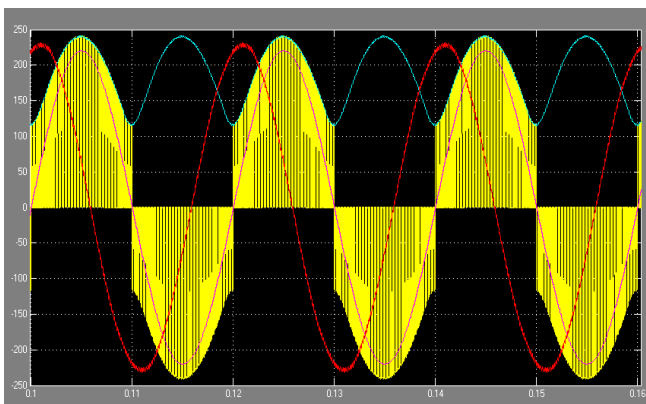
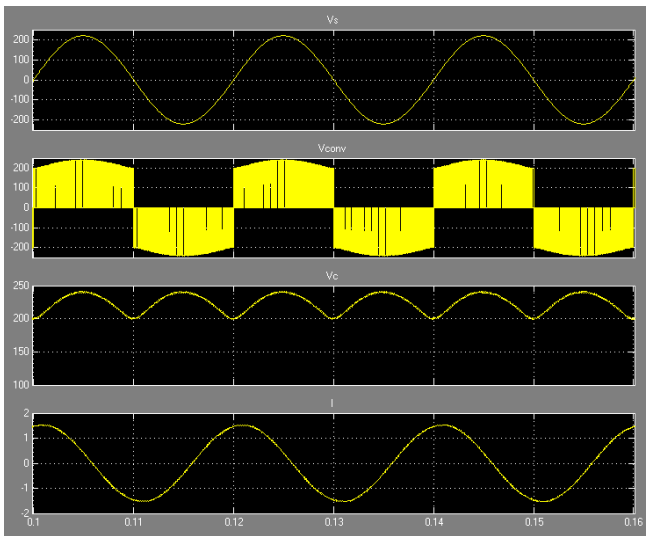
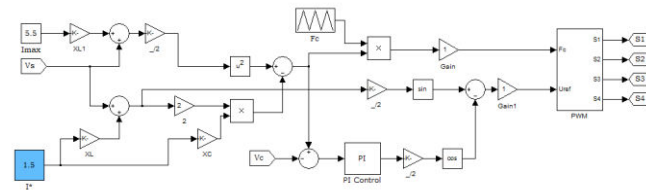
rating STATCOMs, GTO-VSC is still the choice for operation under square-wave mode of switching, that is, once per cycle. A concept of multi-level voltage reinjection in DC circuit of VSC topology, as an alternative to high-frequency device switching adopted under PWM control or instead of adopting higher multi-level topology under FFS principle, has been reported to multiply the pulse-order several times without employing additional VSCs. With commercialization of this approach, there would be a major saving of solid-state devices and magnetic components. A comprehensive review on the STATCOM technology and its development are carried out in this project. The project includes ten sections viz. (i) working principle of STATCOM, (ii) solid-state switching devices and technology, (iii) STATCOM topologies and configurations, (iv) control methodologies and approaches, (v) component selection, (vi) specific applications, (vii) simulation tools, (viii) latest trends and perspective research potentials (ix) concluding remarks and (x) references.

CONTROL STRATEGIES AND APPROACHES

The control system is the heart of state-of-the-art STATCOM controller for dynamic control of reactive power in electrical system. Based on the operational requirements, type of applications, system configuration and loss optimization, essential control parameters are controlled to obtain desired performance and many control methodologies in STATCOM power circuits have been presented in [3]. In a square-wave mode of operation, phase angle control (a) across the leakage reactance (L) is the main controlling parameter. This control is employed in a two-level converter structure, where DC voltage (V_{dc}) is dynamically adjusted to above or equal to or below the system voltage for reactive power control. In a three-level configuration, the dead-angle or zero-swell period (b) is controlled to vary the converter AC output voltage by maintaining V_{dc} constant. The control system for STATCOM

operated with PWM mode employs control of a and m (modulation index) to change the converter AC voltages keeping V_{dc} . For voltage regulation, two control-loop circuits namely inner current control loop and external/outer voltage control loop are employed in STATCOM power circuit. The current control loop produces the desired phase angle difference of the converter voltage relative to the system voltage and in turn, generates the gating pulses, whereas the voltage control loop generates the reference reactive current for the current controller of the inner control loop. This control philosophy is implemented with proportional and integral control (PI control) algorithm or with a combination of proportional (P), integral (I) and derivative (D) control algorithm in d - q synchronous rotating frame. Figs. 3.16 and 3.17 illustrate the PI methodology for two-level and three level GTO-VSC based STATCOM power circuits. The general mathematical approach, modeling and design of control systems for compensator circuits are proposed. In the process of designing and implementation of control system, acquisition of many signals is involved. Initially, the essential AC and DC voltages and current signals (instantaneous values/vectors) are sensed using sensors. In the next step, these signals are synthesized by techniques such as d - q synchronous rotating axis transformation, alpha-beta stationary reference frame of transformation and so on. Phase locked loop circuit is normally employed to calculate phase and frequency information of the fundamental positive sequence component of system voltage which synchronizes AC converter output voltage. Third step involves generation of compensating command signals based on three kinds of state-of-the-art control methodologies, linear, nonlinear and special control techniques. Fourth step is to generate required gating signals for the solid-state devices.

SIMULATION RESULTS



CONCLUSION

In this project a single-phase full-bridge configuration using semiconductor switches with reduced outfitted capacitance designed for reactive power compensation. This work concentrates on reactive power compensators beginning the point of stored energy in the capacitor banks. The new Modulation procedure and the capacitor voltage control technique based on the diminished capacitance and high-voltage ripple within the capacitor are projected. By applying this thought to the shunt-type static var compensator, a static synchronous compensator can be accomplished with reduced-sized

capacitor. Furthermore, the switching loss can be decreased due to its characteristic capacitor voltage waveform, it will swings at the two times of the line frequency. The concept and control method were developed with MATLAB/SIMULINK software and the simulation results are shown good performance of proposed system.

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