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A NOVEL CONFIGURATION OF MPUC7 CONVERTER BASED VOLTAGE BALANCE CONTROL FOR HARMONIC ELIMINATION

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Abstract

Eliminating the specific harmonics especially low-order harmonics of the output voltage of 9-level inverter using SHE-PWM control scheme is investigated. Harmonic minimization is the intricate optimization problems because the nonlinear transcendental equations have multiple local optima. Increasing the degrees of freedom in the suggested method means that the number of switching angles increases. The suggested method is able to eliminate high number of undesired harmonics. As the number of switching angles increases, using either traditional iterative techniques or resultant theory method gets useless. In this paper to overcome this problem the imperialist competitive algorithm (ICA) is used. Also a DC-DC converter is used to enhance the SHE performance in the range for which the conventional SHE methods do not have any solution. Experimental and simulation results of a 9-level inverter show that the proposed method effectively minimizes a large number of particular harmonics so the total harmonics distortion of output voltage will be lower.

1. Introduction

THE developments of FACTS devices [1, 2], medium voltage drives [3, 4], and various types of renewable energy resources [5, 6] have given great opportunities for the implementation of medium- and high-power inverters. The main problem with these applications is the frequency constraint of the pulse width modulation (PWM) which are limited by switching losses and electromagnetic interferences which is the results of high. Thus, to overcome the mentioned problems, selective-harmonic-elimination- (SHE-) based optimal pulse width modulation (OPWM) are proposed which are able to reduce the switching frequency and the total harmonic distortion of

output voltage [7]. A typical multilevel inverter utilizes several DC voltage sources to provide a stepwise waveform in output voltage which makes a great development on output voltage THD while the output waveform approaches nearly sinusoidal waveform [8]. Related to the inverter circuit topologies the dc sources can be interconnected or isolated [7]. Because of the intricacy of the problem, in most studies on the SHE methods for multilevel inverters, it is assumed that only one switching angle per each voltage level is defined and the dc voltage sources are balanced (equal to each other). But in practical applications, depending on the output waveform and

operation scheme of the inverter, the dc sources could be unbalanced or several switchings per each level are involved [9–13]. SHE method is a modulation strategy whose goal is to determine the proper switching angles to eliminate the number of low-order harmonics which cause to minimize the output waveform THD [14]. The SHE method requires low switching frequency and stepwise waveform of output voltages to be applied [15]. The main goal in SHE method is to determine the switching angles in which with the obtained switching angles the fundamental component reaches to the desired value and the undesired harmonics; basically low-order harmonics are eliminated [16]. The defined objective function for SHE problem includes a set of nonlinear transcendental equations which may involve several local optima. Solving the SHE problem is available with the help of several procedures. Resultant theory is a novel which is based on methodical calculations [17]. In resultant theory method the provided equations which are defined for SHE are converted into an equivalent set of polynomial equations, and then resultant theory is applied to the obtained equivalent equations which are naturally high-order polynomials. The main problem with this method is the complexity of calculation. The complexity increases when the number of switching angles is increased and so makes the equivalent equation harder to be solved or even not to be solved. Another approach for solving is the Newton-Raphson method, and it is a Numerical iterative technique [18]. However, the great difficulty of these techniques is the proper initial guess requirement that should be near to the exact

solution. It is evident that giving a proper guess is very difficult in most cases but if a proper initial guess is available, Newton-Raphson method works properly. This difficulty is the result of the SHE problems search space which is unknown for anybody, and no one knows whether a solution exists or not, and if exists, what is the proper initial guess? A recently developed novel method to deal with the SHE problem is based on evolutionary algorithms such as Bee algorithm [19]. However the search spaces intricacy would increase vividly if the number of switching angles increases and both methods fall into the trap of local optimum points of search space. Surely, the precise limitation for the number of switching angles cannot be determined in evolutionary algorithms. So, increasing the number of switching angles reduces the probability of finding the optimum switching angles, unfortunately. The SHE-PWM is a novel method that provides more number of the degrees of freedom (DOFs) and makes available to eliminate more harmonic components with no need to change the perceivable hardware of the inverter [20, 21]. In SHE-PWM, each active device can be switched at least twice per cycle, and larger number of harmonic components than in the case of fundamental frequency switching scheme can be eliminated. The major problem related to the SHE-PWM method is that as the number of switching angles is increased; none of aforementioned methods can be applied to determine the switching angles. In this paper, a novel method based on ICA is developed to deal with the SHE-PWM problem. Simulation and experimental results are obtained for a 9-level cascaded multilevel inverter to validate

the efficiency of the proposed method and accuracy of the authors. The rest of this paper is organized as follows.

Section 2 describes the multilevel inverters output voltage and its harmonic spectrum. Section 3 defines the SHE approach objective.

In Section 4, the obtained simulation results are presented. Also to improve the capability of the SHE, an efficient solution is presented. Finally, in Section 5 experimental results are provided to confirm the author's accuracy. Section 6 is specified for the conclusion.

2. Output Voltage of Multilevel Inverter

A half cycle of a typical waveform of the reference phase voltage of a 9-level inverter synthesized by several DC sources using SHE-PWM method is illustrated in Figure 1.

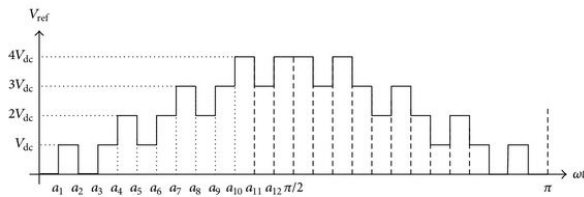


Figure 1: Half cycle of the reference phase voltage of a 9-level inverter.

The $a_1 - a_{12}$ are the required switching angles to specify the whole cycle of the shown waveform. Regarding Figure 1 the reference voltage, V_{ref} can be demonstrated in terms of step function $u(\omega t)$.

For positive half of the waveform:

$$V_{ap} = u(\omega t - a_1) - u(\omega t - a_2) + u(\omega t - a_3) + \dots + u(\omega t - a_{10}) - u(\omega t - a_{11}) + u(\omega t - a_{12}) \quad (1)$$

$$0 < \omega t < \pi.$$

The negative half is in the opposite direction and lagging V_{ap} in π radians:

$$V_{an} = -V_{ap}(\omega t - \pi); \quad \pi < \omega t < 2\pi. \quad (2)$$

Therefore the sum of (1) and (2) affords the reference phase voltage illustrated in

$$V_a = V_{ap} + V_{an} \quad (3)$$

To identify instantaneous value of reference phase voltage the Fourier analysis is applied and the following expression is obtained. If

the presented waveform in Figure 1 was considered for one cycle, it can be seen that it is an odd function and so it will contain only odd-order harmonics:

$$V_a = \sum_{n=1}^{\infty} \frac{4}{n\pi} V_{dc} \sin\left(\frac{n\pi}{2}\right) \times (\cos(na_1) - \cos(na_2) + \cos(na_3) + \cos(na_4) - \cos(na_5) + \cos(na_6) + \cos(na_7) - \cos(na_8) + \cos(na_9) + \cos(na_{10}) - \cos(na_{11}) + \cos(na_{12})) \sin(n\omega t). \quad (4)$$

In (4), the negative signs indicate the falling edges of the staircase voltage waveform and vice versa. The objective of SHE-PWM method is aimed to choose the set of switching angles such that the identified lower-order harmonics are suppressed, and, at the same time, the amplitude of fundamental component becomes equal to the desired value.

3. Selective Harmonic Elimination

In most papers, the staircase voltage waveform is considered in SHE method in multilevel inverters [22, 23]. In this case, for a N -level inverter, the number of switching in a quarter of cycle is limited to $N-1$, where N is the number of individual DC sources. So the number of harmonics which can be eliminated from the output voltage is $N-1$. In order to increase the DOFs and eliminating more harmonics than the case of fundamental frequency switching scheme without any manipulation on inverters hardware, the SHE-PWM technique is proposed in [12], which is also denominated virtual-stage PWM. This technique is one of the powerful theories that applies to multilevel inverters in order to generate high-quality voltage waveform with less switching frequency in comparison with other PWM methods. The generalized

formula to eliminate lower-order harmonics in multilevel inverter with nonequal dc sources is also suggested in [21]. Commonly, the number of switching can vary in different levels. However, for simplicity, the number of switching is evaluated to be equal in this paper for different levels. Figure 1 presents the output voltage of 9-level inverter for three times of switching at each level. If indicates the number of switching at each level, switching frequency of the SHE-PWM scheme will be times the fundamental frequency. Therefore, the number of harmonic components that can be eliminated from the output voltage is evaluated by for the 9 level. For , the output voltage that is shown in Figure 1, there are 12 DOFs, and so 11 undesired harmonics can be eliminated from the output voltage of inverter. The SHE PWM method is able to eliminate the nontriple lower-order harmonics up to when is odd and up to order harmonic while is even. It is noticeable that eliminating the triple harmonics is not necessary for three-phase applications since these harmonics should automatically be removed from the line-to-line voltage. A 9-level inverter is defined as a case study to assess the impact of SHE-PWM on its harmonic spectrum. Objective function for a 9-level inverter is the result of combination of 12-nonlinear equations that one equation is for fundamental component and the other equations are related to the undesired harmonics.

The mentioned equations are presented as follows:

$$\begin{aligned}
 (V_{a1}) &= \frac{4}{\pi} V_{dc} (\cos(a_1) - \cos(a_2) + \cos(a_3) \\
 &\quad + \dots - \cos(a_{11}) + \cos(a_{12})), \\
 (V_{a5}) &= \frac{4}{5\pi} V_{dc} (\cos(5a_1) - \cos(5a_2) + \cos(5a_3) \\
 &\quad + \dots - \cos(5a_{11}) + \cos(5a_{12})), \\
 &\quad \vdots \\
 (V_{a35}) &= \frac{4}{35\pi} V_{dc} (\cos(35a_1) - \cos(35a_2) + \cos(35a_3) \\
 &\quad + \dots - \cos(35a_{11}) + \cos(35a_{12})).
 \end{aligned} \tag{5}$$

Satisfying the fundamental component and eliminating undesired harmonics in the same time is considered as an objective function. Given the previous descriptions, the objective function is given by the following expression:

$$\text{objective: } \left(100 \frac{V_1^* - V_1}{V_1^*} \right)^4 + \sum_{i=2}^{KS} \frac{1}{h_i} \left(50 \frac{V_{h_i}}{V_1} \right)^2, \tag{6}$$

$i = 1, 2, \dots, KS,$

$$V_1^* = \frac{V_1}{V_{dc}}, \tag{7}$$

where is the possible normalized amplitude of desired fundamental component which is denominated as the modulation index for this objective function varies from zero to , is the number of switching angles, and is the order of th viable harmonic at the output of a three-phase multilevel inverter, for example, , and . If the fundamental component transgresses its set point by more than 1%, the first term of (6) defines it by a power of 4. Because of using the power of 4, corresponding penalties for any digressions under 1% get a negligible value. The second term of (6) neglects harmonics under 2% of fundamental component. But, when any harmonic exceeds this limit, the objective function is subject to a penalty by power of 2. Finally, each harmonic ratio is weighted by inverse of its harmonic

order, that is, . With this weighting method, reducing the low-order harmonics gets higher importance. The switching angles which are the solutions of aforementioned objective function must gratify the following basic constraint:

$$0 \leq a_1 \leq \dots \leq a_{12} \leq \frac{\pi}{2}. \quad (8)$$

In this paper imperialist competitive algorithm is used to solve the defined problem. Imperialist competitive algorithm is a recently developed evolutionary optimization method which is inspired by imperialist competition. Like other evolutionary algorithm, to solve a problem, it requires an initial population in which finally one of them will be the proper solution of aforementioned problem. Initial population is the sum of countries which is divided into imperialists and colonies which together form empires. One imperialist with relevant colonies creates an empire. Based on assimilation policy, the colonies start moving toward their relevant imperialist state while improving their economy, culture, political, and social situation. The total power of an empire is the sum of imperialist power and the percentage of relevant colonies power. During the mentioned competition, all empires attempt to take possession of colonies of other empires until weak empires collapse and just one most powerful empire exists which the Imperialist and the relevant colonies have the same value of cost function. So, the state with most powerful empire is the convergence point of ICA. Figure 2 depicts the flowchart of ICA. With regard to the concept of SHE, variables are the switching angles of the 9-level inverter.

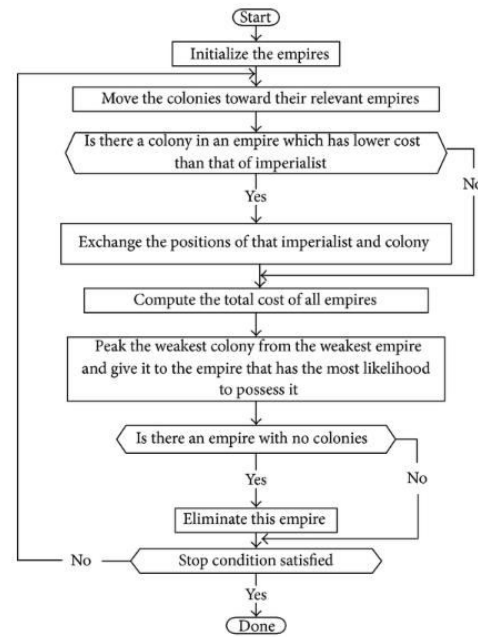
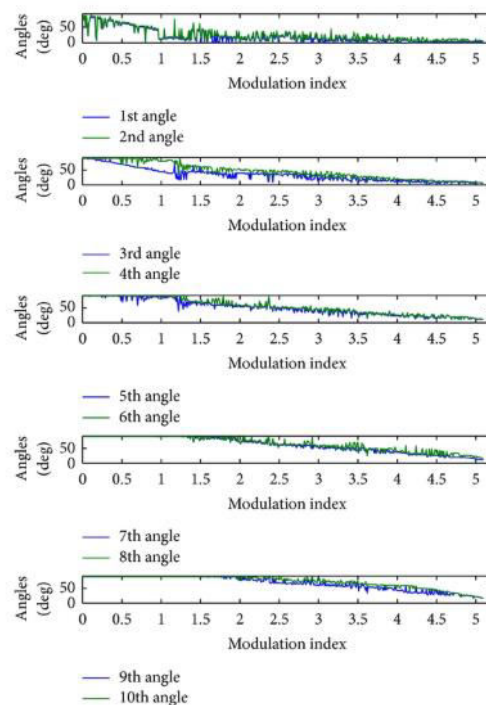


Figure 2: The flowchart of ICA process.

4. Simulation Results

The SHE method is applied to the nine-level inverter and the results are obtained. The corresponding optimum switching angles versus the modulation index are represented in Figure 3(a).



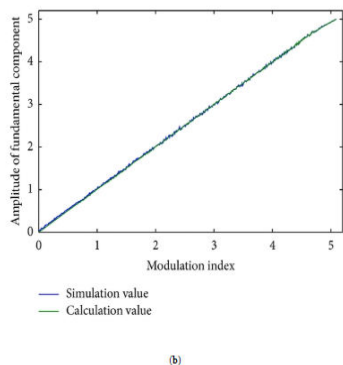


Figure 3: (a) Optimal switching angles versus modulation index. (b) amplitude of the fundamental component versus modulation index.

Figure 3(b) shows the amplitude of the fundamental component versus modulation index. The normalized amplitude of undesired harmonics versus modulation index is plotted in Figure 4. It can be seen from this figure that the amplitude of undesired harmonics has very low values and so the presented method can properly eliminate the selected harmonic components. Figure 5 illustrates the amount of cost function with respect to the modulation index.

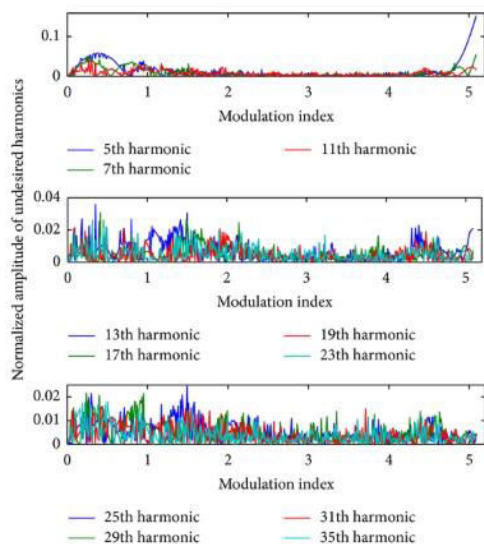


Figure 4: The normalized amplitude of undesired harmonics ($V_h/4V_d$) versus modulation index.

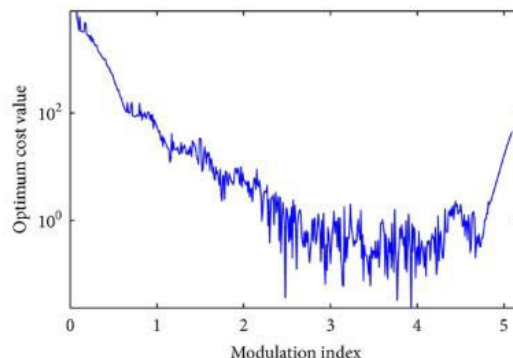


Figure 5: The optimum value of cost function versus modulation index.

The obtained solutions for the switching angles and the corresponding cost values are presented in Figures 3(a) and 5, respectively. From the cost values shown in Figure 5, it is apparent that the ICA algorithm is effectively able to find the optimum switching angles to meet the objective function which aims to achieve the fundamental component and suppress the undesired harmonics. As illustrated before, there is no possibility to meet the objective function in all ranges where SHE is applied. As shown in Figure 5 it is clear that for some ranges of modulation index the value of cost function is low and it means that all proposed undesired harmonics get nearly to zero. The ranges of with high values of cost function mean that the values of harmonic components are nonzero, and so the equations cannot be solved and there is no solution. Mostly in the whole ranges of modulation index, shown in Figure 3, the fundamental component is kept close to the desired value. Based on presented simulation results it can be seen that there are some regions of modulation index in which the optimum value of switching angles do not exist. To overcome this problem a DC/DC inverter is used in DC link of inverter. This converter controls the DC link voltage of inverter, . Figure 6 shows the overall systems

content, includes the 9-level cascade inverter, the buck converter, and the required control unit to coordinate switching pattern with the input DC voltage of the 9-level cascade inverter.

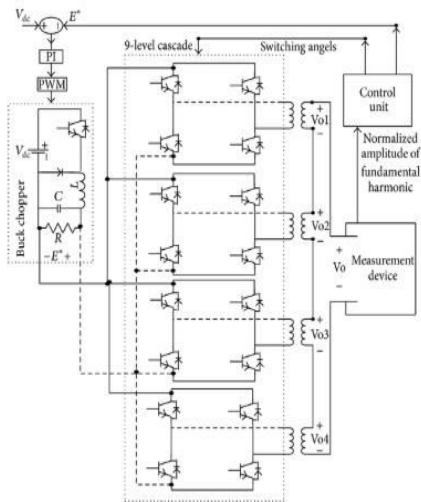
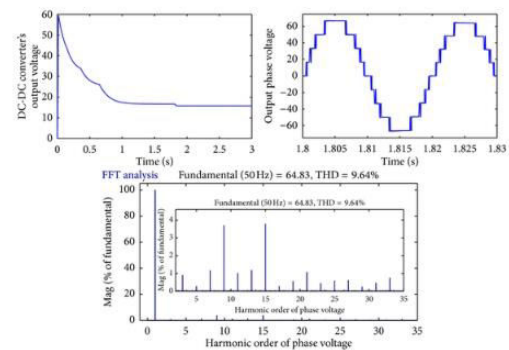


Figure 6: The overall system consists of the 9-level cascade inverter, the buck converter, and the required control units.

It can be seen from Figure 5 that there are not proper solutions for switching angles in all modulation indexes. Therefore, when the fundamental harmonic amplitude is in region without solution, the control unit sends a signal, includes required input DC voltage, of the inverter, to the DC-DC converter, and modifies the exciting switching pattern based on new DC link voltage and sends it to the inverter. Following expression is the basic constraints must be gratified by control unit:

$$[(\cos(a_1) - \cos(a_2) + \cos(a_3) + \cos(a_4) - \dots - \cos(a_{11}) + \cos(a_{12}))]^{NEW} * E^{*NEW} = \frac{M}{(4/\pi)} \quad (9)$$

$$0.2V_{dc} \leq E^{*NEW} \leq V_{dc} \quad (10)$$



5. Experimental Results

To confirm the validation and effectiveness of presented method in this paper, an experimental prototype of a single-phase nine-level cascaded inverter with a buck converter in DC side of inverter has been implemented. Figure 8 shows the implemented prototype. Parameters of implemented circuit are given in Table 2. Figure 9 depicts the experimental results of implemented circuit. It can be seen from Figures 9(a)–9(c) that the experimental voltage waveforms are more similar to simulation results and the harmonic spectra of mentioned waveforms validate the implemented methods efficiency. Also, in Figure 9(c) the output voltage of DC-DC converter is shown. It can be seen from this figure that the DC-DC converter can properly track the reference voltage.

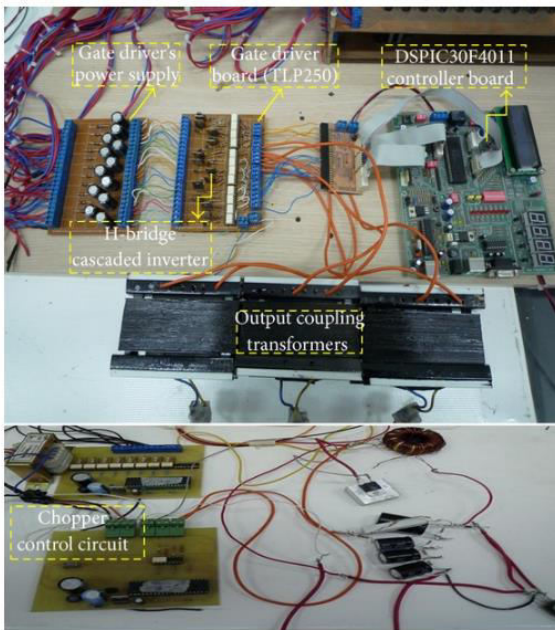


Figure 8: The implemented prototype setup.

6. Conclusion

An ICA algorithm has been suggested to determine the optimum switching angles of a 9-level inverter. This algorithm has been successfully exerted to the SHE-PWM problem with larger number of switching angles, where other traditional methods cannot solve it. Simulation and experimental results are provided for a 9-level inverter to confirm the accuracy of suggested method. Results show that the defined undesired harmonics have been successfully minimized at the output voltage waveform. Also to develop the sufficiency of the SHE to all range of modulation index a DC-DC converter is used to control the input DC voltage of the inverter which dramatically improves the SHE effectiveness on eliminating the undesired harmonics.

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