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MLI BASED PHOTOVOLTAIC SYSTEMS FOR SINGLE PHASE GRID CONNECTED SYSTEM

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ABSTRACT:

In this project MLI based Photovoltaic systems for single phase grid is proposed. Photovoltaic systems require interfacing power converters between the PV arrays and the grid. These power converters are used for two major tasks. First, is to inject a sinusoidal current in to the grid. And second is to reduce the harmonics content in the grid injected voltage and current. Normally there are two power converters. The first one is a DC/DC power converter that is used to operate the PV arrays at the maximum power point. The other one is a DC/AC power converter to interconnect the photovoltaic system to the grid. Multilevel converters enable the output voltage to increase without increasing the voltage rating of the switching devices, so that they offer the direct connection of renewable energy systems to the grid voltage without using the expensive and bulky transformers. This project proposes photovoltaic system with a multilevel inverter connected to a grid for getting better performance which is shown by using MATLAB/SIMULINK model.

Keywords— photovoltaic energy; grid-connected; voltage oriented control; single phase; inverter.

1. INTRODUCTION

Addressing the ever increasing global demand on reliable and sustainable electricity has become a main concern of electricity sector. At the moment, fossil fuel power plants are the backbone of world's electricity generation system. On the other hand, they are recognized as a major cause for the environmental pollutions. Today, millions are willing to enjoy the benefits of improved lifestyle and environment with much more electricity generated from the renewable energy sources. In 2010, renewable energies accounted for 16.6% of the world's primary energy demand, while a share up to 50% worldwide is feasible by 2040, according to a report published by the European Renewable Energy Council (EREC) together with its member associations (EPIA, ESHA, ESTIF, EUBIA, EUREC Agency, EWEA, AEBIOM and EGEC). Along with ambitious plans to increase the share of renewable energies in developed countries, such as EU 2020 political renewable energy goal,

and progresses in renewable energy utilization in developing and newly industrializing countries, small-scale electricity generation systems, such as small hydro turbines, roof mounted photovoltaic and wind generation systems, and commercially available fuel cells are being widely utilized at the distribution level. The general structure of a small-scale renewable energy source, which is usually interfaced with the single-phase grid through a stage of power electronic devices, is depicted. The source side power conversion system extracts the maximum electricity from the available renewable input power and its topology and control strategy highly depends on the type of the renewable energy source.

The focus of this paper is on the grid-side converter. The main objective of the grid-side converter is to control the power flow between the ac system and the dc link powered from the renewable resources. The single-phase voltage



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source converter (VSC) is the best solution for interfacing with a single-phase grid. It offers several advantages such as bidirectional power flow capability and low distortions at the ac side current and the dc side voltage. Another promising feature is that it can independently control the active and reactive power exchanged with the ac network; therefore, it can improve the voltage profile, and is able to operate in weak ac systems. These advantages make the single-phase VSC a successful solution for grid integration of small-scale renewable energy sources. To regulate the power exchange with the single-phase grid, and at the same time, reduce the harmonic distortions in the ac current, different current control structures have already been proposed, such as current hysteresis control (CHC), voltage oriented control (VOC), and proportional-resonant (PR) based control, which all have their own pros and cons.

This paper gives an overview of the main characteristics of these control strategies. The paper also includes a discussion of some implementation aspects such as the fictitious signal generation and the single-phase grid synchronization techniques. Then, through extensive simulations a comparative study of the presented control strategies is presented and discussed. The simulations are supported by experiments.

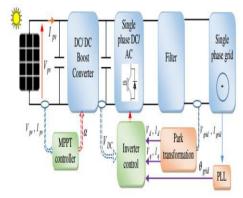


Fig.1 Scheme of the proposed photovoltaic system connected to a single-phase grid utility

(II) SYSTEM MODELLING

To have an efficient control strategy, models for the system components are needed. Hence, in this section, the system components models are presented.

A. Photovoltaic panels

A one-diode based non-linear model is used to model the photovoltaic panels, using an ideality factor to describe the diode's performance [8, 9]. The model uses the radiation G(t), the ambient temperature $T_a(t)$ at the panel surface, and the panel parameters to evaluate the photovoltaic current $I_c(t)$ [10]. The adopted model is described by (1)-(6) [10, 11]:

$$a \frac{V_c(t) + R_s i_c(t)}{V_t}$$

$$\exp(-1) - \left(\frac{V_c(t) + R_s i_c(t)}{R_p}\right)^{(1)}$$

$$I_c = I_{ph}(t) - I_r(t)$$

$$I_{ph}(t) = \frac{G(t)}{G_{ref}} I_{sc}(t)(2)$$

$$ref$$

$$I_{sc}(t) = I_{sc} \frac{(1 + a(T_a(t) - T_{ref}))}{(1 + a(T_a(t) - T_{ref}))}(3)$$

$$I_r(t) = I \frac{1}{r \left(\frac{T_a(t)}{T_{\text{ref}}}\right)^{\frac{3}{n}} \exp\left(\frac{-qV_g}{nK_g}\left(\frac{1}{T_a(t)} - \frac{1}{T_{\text{ref}}}\right)\right)}$$
(4)



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where:

 $T_a(t)$: the ambient temperature at the panel surface (K),

G(t): the solar radiance at the panel surface (W/m^2) .

 $I_c(t)t$: the estimated photovoltaic cell current (A),

 $I_{\rm ph}(t)t$: the generated photo-current at G(A),

 $I_r(t)$: the reverse saturation current at $T_a(A)$,

 $V_c(t)$: the open circuit voltage of the photovoltaic cell (V),

 R_s : the serial resistance of the photovoltaic module (Ω) ,

 V_{t-T_a} : the thermal potential at the ambient temperature (V),

 R_p : the parallel resistance of the photovoltaic module (Ω) ,

 G_{ref} : the solar radiation at reference conditions (W/ m2),

 $I_{\rm sc}(t)$: the short circuit current for $T_a(A)$,

 $\underset{I_{sc}}{\text{ref}}$:the short circuit current per cell at the

reference

temperature $T_{ref}(A)$,

a: the temperature coefficient for the short circuit current ($K\Box 1$),

 $\frac{\text{ref}}{I_r}$: the reverse saturation current for $T_{\text{ref}}(A)$,

n: the quality factor,

q: the electron charge (C),

Vg: the Gap energy (e.V),

KB: the Boltzmann coefficient (J/K),

ref V_c : the open circuit voltage per cell at $T_{ref}(V)$.

The photovoltaic power P_{pv} generated by the panel is then described as follows [11-13]:

$$P_{pv}(t) = n_{s}n_{p}V_{c}(t)$$

$$a$$

$$\frac{V_{c}(t) + R_{s}I_{c}(t)}{V_{t}}$$

$$\exp(-1)$$

$$-\frac{V_{c}(t) + R_{s}I_{c}(t)}{R_{p}}$$

$$righ$$

$$I_{ph}(t) - I_{r}(t)($$

where:

ns: the number of serial photovoltaic cells,n p: the number of parallel photovoltaic modules.

B. Incremental conductance for MPPT Thanks to its efficiency, the incremental conductance algorithm for MPPT is used to track the Maximum Power Point (MPP) of the photovoltaic panels, and generate the control signal for the boost converter [14] (Fig. 2). In fact, this method uses the current ripple in the chopper output I pv to maximize the panel power Ppv using the relation between the current and voltage continuously identified online [15, 16].

Indeed, the algorithm tests the actual conductance -Ipv/Vpv and the incremental conductance dIpv/dVpv as follows [13, 17, 18]: Thus, the duty cycle α , used to generate the PWM signal for the MOSFET switching, is calculated using (10) [20]:

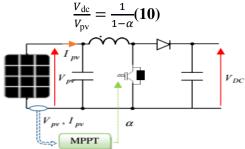


Fig.2 Control principle of the boost converter for the MPPT

C. Single- phase DC/AC inverter

A full bridge inverter is used to convert DC voltage and current to AC voltage and current (*Fig.* 3). The inverter is controlled by a PWM



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signal, which allows the control signals S1 and S2 to be deduced. In fact, this modulation obtained by comparing referential sinusoidal signals called modulating waves (characterized by the frequency fr and the amplitude Vr) with a triangular signal (called carrier wave, characterized by a frequency $fp \square \square \square fr$, and the amplitude Vp [21]. The first leg is controlled by a signal $V \bmod ulation$, while the second leg is controlled by $\square V \bmod ulation$. When the two signals take the same value, the IGBTs change the state, so PWM signals are generated, with the frequency fp [21]

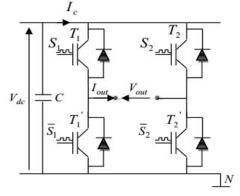


Fig.3 Single-phase full bridge inverter The switches T1,T1', T2 and T2' are connected as follows:

- \Box If V mod u lation \Box \Box V carrier, then T1 is on
- If $V \bmod ulation \square \square V carrier$, then T1' is on
- If $\Box V \mod ulation \ \Box \ \Box V carrier$, then T2 is on.
- \Box If \Box V mod ulation \Box \Box V carrier, then T2 ' is **on**.

Hence, the inverter output voltage is evaluated by (Fig.4):

$$V_{\rm out}(t) = V_{\rm AN}(t) - V_{\rm BN}(t)(11)$$

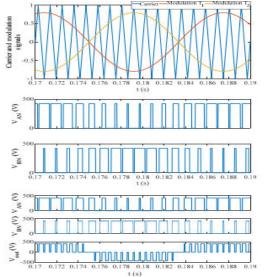


Fig.4 PWM based control for single- phase inverter

(III) CONTROL STRATEGY PRINCIPLE

A. Inverter Converter

The voltage-oriented control or VOC regulates the active and reactive currents by orienting the current vector with respect to the line voltage vector. In fact, VOC is performed in the directquadrature (d, q) synchronous reference frame, where the error between the direct (Id) and quadrature (Iq) components of the AC current and their reference values are fed to the PI controllers [22]. Then, the regulator generates the reference voltage for converter, which is applied to the AC converter using a PWM modulator. Hence, all electrical signals are transformed to the synchronous reference frame, where quantities are DC and, as a consequence, the zero steady-state error is ensured by using a conventional proportionalintegral (PI) controller (Fig. 5).

To perform the VOC, two orthogonal signals are needed. As a consequence, a fictitious phase must be generated. To create a two phase system from a single phase signal, the transport delay method is used here [23].



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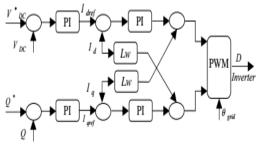


Fig.5 Simplified block-diagram of VOC based control of single-phase grid-tie converters

B. Fictious phase generation

The single-phase circuit has one phase conductor with a neutral return. In order to extend the synchronous (d, q) reference frame control strategies to single-phase systems, two orthogonal signals are needed. Hence, a second fictitious phase should be properly generated, to model a single-phase circuit as an equivalent virtual two-phase circuit. In the literature, there are several successful works for generating the fictitious phase including the transport delay [24], Hilbert transformation [25, 26], all-pass filter [27], and SOGI [28] methods. In this paper, the transport delay method is used to generate the fictious phase, since it is simple and can be implemented by digital controllers without the need for tuning any parameters [28] (Fig. 6). Indeed, a phase shift of 90° with respect to the fundamental frequency of the input signal is realized through the use of a firstin-first-out memory, with the delay adjusted to one fourth the number of total samples of a cycle of the fundamental frequency [28].

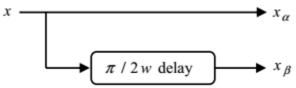


Fig.6 Transport delay method to generate the fictitious phase signal

C. Single phase synchronization

Among several synchronization techniques, the phase locked loops (PLLs) are the most used technique for synchronization, thanks to its

simplicity, robustness, and effectiveness. In fact, the PLL is a closed loop feedback control system, which synchronizes its output signal in phase, as well as in frequency, with the fundamental component of the grid voltage. Among various techniques, currently, the synchronous reference frame PLL (SRF-PLL) is the most common technique used for single phase grid synchronization [28] (*Fig.* 7).

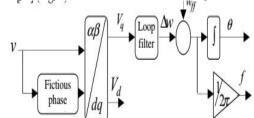


Fig.7 Control strategy for the grid-connected photovoltaic panel

D. Current control loop

The current control loops are used to control the direct and quadrature grid currents, *Id* and *Iq*, respectively. In fact, these components are compared with the respective reference component *Idref* and *Iqref*, and the resultant errors are used by PI regulator to generate the inverter control signal, which allows the AC signals to be generated, following the grid characteristics (frequency and phase) [23].

Indeed, the direct current component controls the grid active power flow, while the q component is used here to regulate the reactive power flow. Hence, a voltage loop, which regulates the mean and measured voltages at the Boost output, is used to generate the reference direct current *Idref* (Fig. 5). While, the quadrature current reference *Iqref* is generated by the grid phase control loop (Fig. 8).

The DC bus voltage is maintained constant by a voltage loop, which allows the continuous active power flow between the PV array and the grid, to be maintained. Thus, a PI regulator uses the difference between the reference and measured signals of the DC voltage to generate the reference signal of the direct current $I_{d_{\text{ref}}}(12)$ [23]:



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$$C_{\rm DC} \frac{\mathrm{dV}_{\rm DC}}{\mathrm{dt}} = \frac{P_{\rm pv} - P_{\rm grid}}{V_{\rm DC}} (12)$$

E. Reactive power control

The reactive power is controlled by the power coefficient control. In fact, the reactive power control is ensured by generating the reference signal of the reactive current component $I_{q_{\rm ref}}$, thanks to a PI regulator. Fig. 8 shows the simplified block diagram of the reactive power control loop.

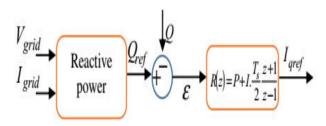


Fig.8 Block diagram of the reactive power control loop

(IV) FIVE LEVEL INVERTER

Five Level Inverter for Grid Connected Application

A DC-AC inverter is employed that generate a sinusoidal current compiled with international standard to be either injected into grid or to fed local and remote load. A five-level bidirectional converter is developed and applied for PV integration to the power grid. The converter is configured by two dc capacitors, a three converter part, an unfolding bridge and a filter. It will operate in two modes, rectifier mode and inverter mode. During the start-up, the grid acts as the source and the 5works in rectifier mode.

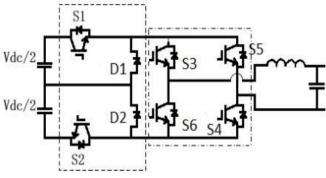
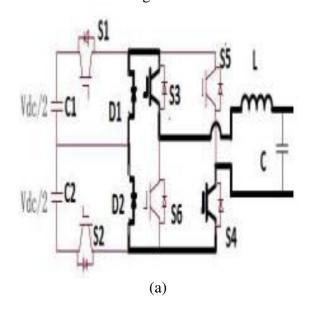


Figure 9: Five level inverter

After the PV optimizer stage starts, the converter will automatically work in inverter mode and generate an output voltage with five levels. In both modes, only two high frequency switches will operate and the unfolding bridge will work at grid frequency. The Five level inverter is developed from this bidirectional converter, considering only the inverter mode of operation. With the control strategy, the dc link capacitor voltages are balanced and therefore only source is required compared to other multilevel topology and the number of switches is reduced compared to the diode clamped, flying capacitor and cascade H -bridge 5 level inverters with the same voltage levels.

Operating Modes

The operation of the five-level converter can be divided into rectifier and inverter mode. The converter operates mainly in inverter mode as a grid-tie inverter. In this mode, only S1 and S2 operate at high frequency. The switches S3 and S4 will be ON only during positive half cycle and S5 and S6 will be ON only during the negative half cycle. The inverter mode could be further divided into eight modes.





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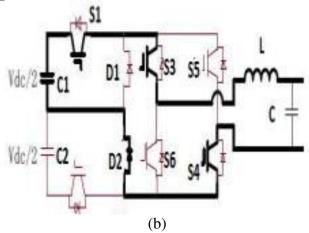


Fig 10. Modes of Operation of 5 level inverter (a) Mode 1 (b) Mode 2

a. INV Mode 1

Figure 10 (a) shows operation circuit of INV Mode 1. Both Sand S2 are turned off. The current of the filter inductor flows through S3, utility, S4 and the two body diodes of D1 and D2 The inverter output is 0.

b. INV Mode 2

Figure 10 (b) shows operation circuit of INV Mode 2. S1 is turned on and S2 is turned off. DC capacitor C1 is discharged through S1, S3, filter inductor, utility, S4 and the body diode of S4 to form a loop. The inverter output is Vdc/2.

c.INV Mode 3

Figure 11(a) shows operation circuit of INV Mode 3. Both Sand S2 are turned on. DC capacitors C1 and C2 are both discharged through S1, S3, filter inductor, utility, S4 and S2. The inverter output is Vdc.

d.INV Mode 4

Figure 11(b) shows operation circuit of INV Mode 2. S1 is turned off and S2 is turned on. DC capacitor C2 is discharged through the diode of D1, S3, filter inductor, utility, S4 and S2to form a loop. The inverter output is Vdc/2.

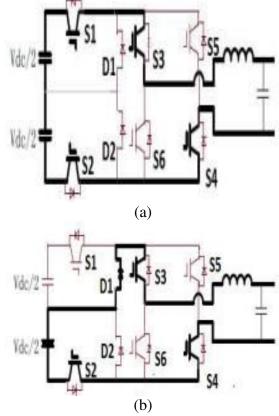


Figure 11. Modes of operation (a) Mode 3 (b) Mode 4

The operations of the converter under INV mode 5-8 are similar to that under INV mode 1-4. The inverter output is 0,Vdc/2 and Vdc, respectively. The inverter therefore, can have a five-level voltage output, which is Vdc, Vdc/2, 0,Vdc/2 and Vdc. The PWM strategy is used in the control section which helps balance the DC link capacitors. In PWM strategy, S1 and S2 will be turned on, turned off, turned on alternatively or turned off alternatively. The voltage balance issue is important for multilevel inverter. If the capacitor voltages are not balanced, the output voltage may become unsymmetrical and result in high harmonics in load current. No shoot through issues for the two high frequency switches, and no dead zone needed, which reduces the harmonics of the output and simplifies the control.



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(V) SIMULATION RESULTS

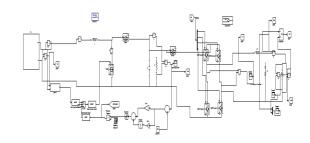


Fig 12 Simulink diagram of proposed concept

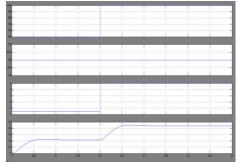


Fig.13 Photovoltaic panel characteristics

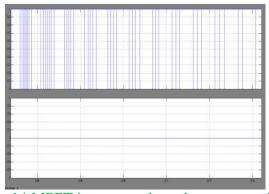
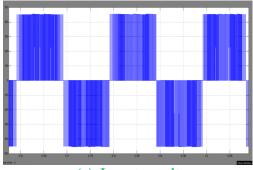


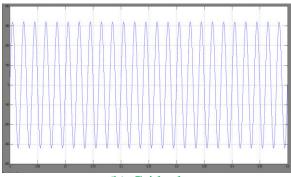
Fig.14 MPPT incremental conductance results



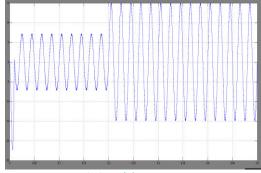
Fig.15 Output voltage at the boost converter terminals



(a) Inverter voltage



(b) Gridvoltage



(c) grid current



(d)Modulation Index



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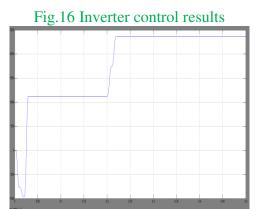


Fig.17 Active power control results

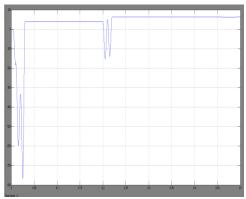


Fig.18 Reactive power control results

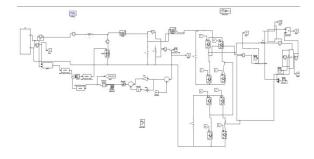


Fig 19 Simulink diagram five level converter

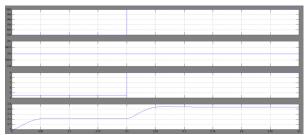


Fig.20 Photovoltaic panel characteristics

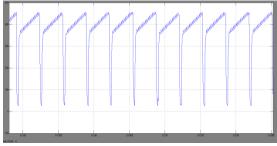


Fig.21 Output voltage at the boost converter terminals

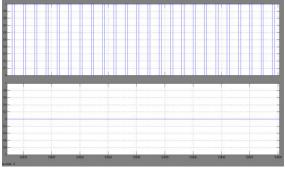


Fig.22 MPPT incremental conductance results



(a) direct axis Id vs time (t)

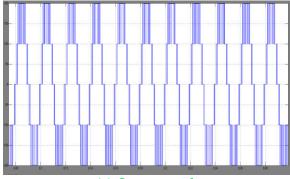


(b) quadrature axis (iq) vs time Fig.23 Direct and quadrature currents characteristics

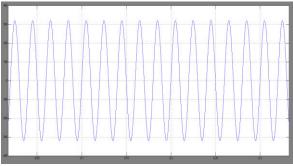


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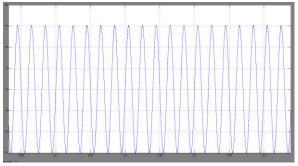
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(a) Inverter voltage



(b) Grid voltage vgrid



(c) grid current Igrid



(d) Modulation index vs time Fig.24 Inverter control results

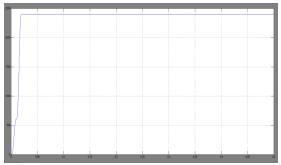


Fig.25 Active power control results

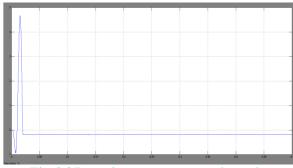


Fig.26 Reactive power control results

(VI) CONCLUSION

In this paper, a single- phase inverter control is studied in depth. The control strategy uses the voltage oriented control to control the active and passive powers. Following the above conducted studies, one can conclude that VOC presents good results in controlling a single-phase grid connected inverter. In a future work, the authors will present the effect of intermittent changes for the climatic parameters in the performance of single- phase grid connected inverters.

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